



Update on XFEL LLRF system development and production

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for the LLRF team



MSK – LLRF team – MANY THANKS

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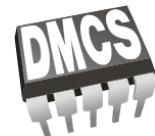
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■ National Centre for Nuclear Research, Świerk, Poland

Grzegorz Bołtruczyk, Stefan Korolczuk, Maciej Kudła, Jarosław Szewiński

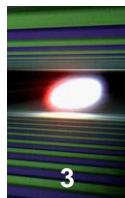


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Krzysztof Oliwa, Wojciech Wierba



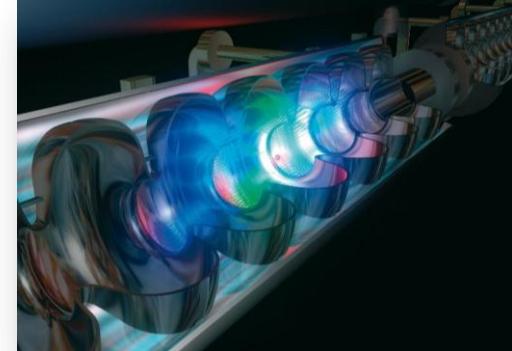
OVERVIEW



3

■ XFEL and LLRF

- XFEL accelerator overview
- LLRF system overview in 1 (big) slide



■ A few highlights

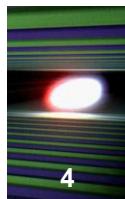
- Hardware
- Operation



■ Schedule and status

- XFEL schedule
- LLRF production
- Outlook



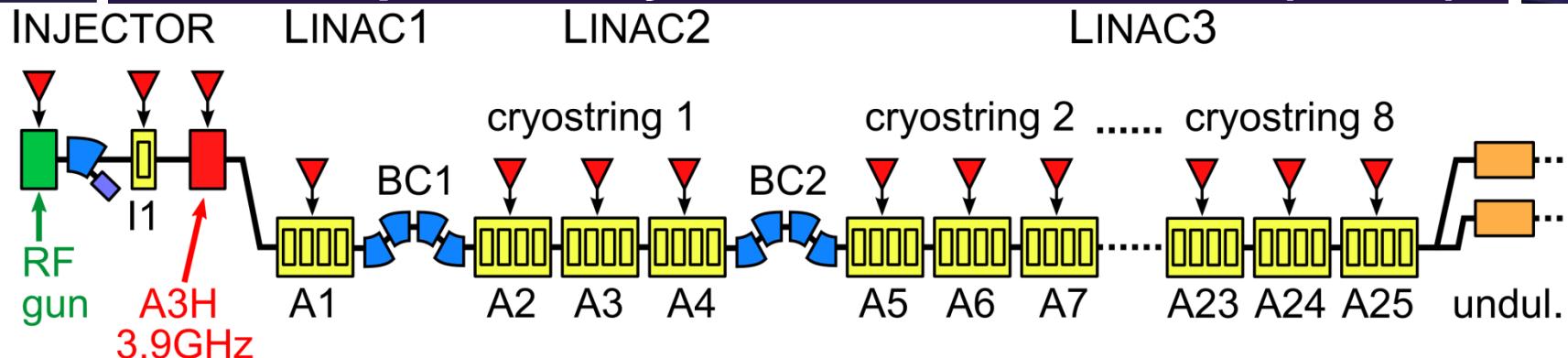


INTRODUCTION

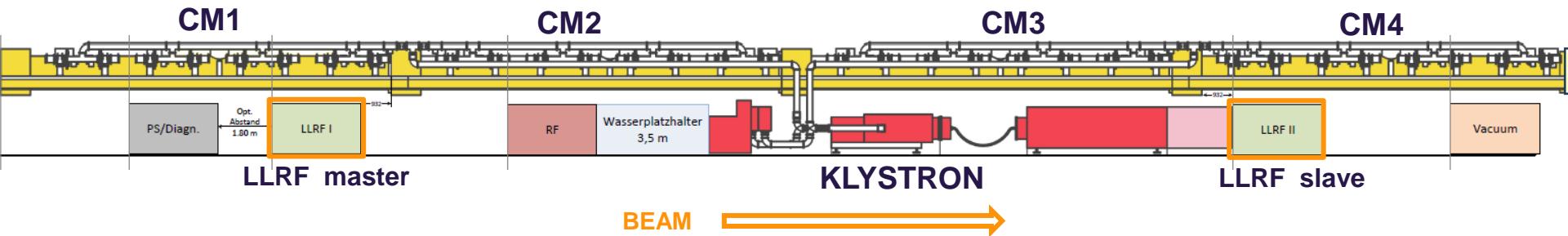
XFEL accelerator overview

LLRF system overview

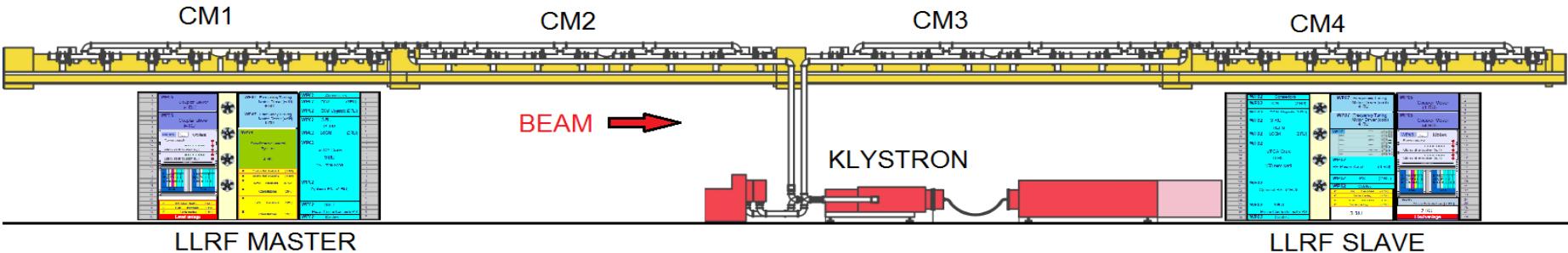
The European X-ray Free-Electron Laser (XFEL)



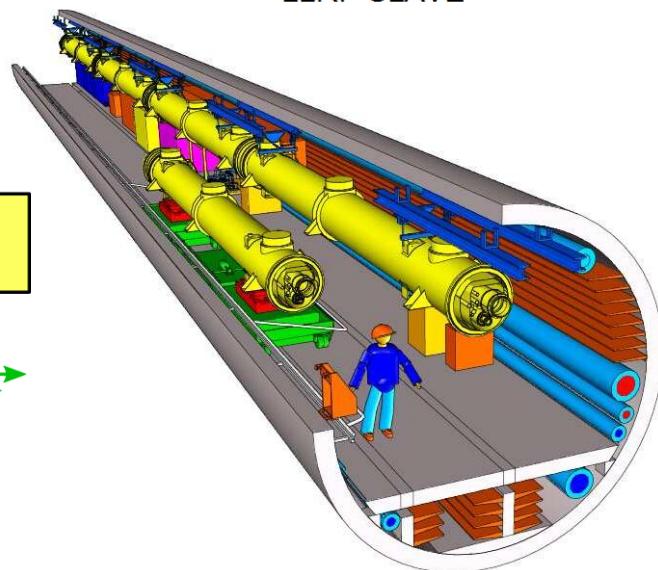
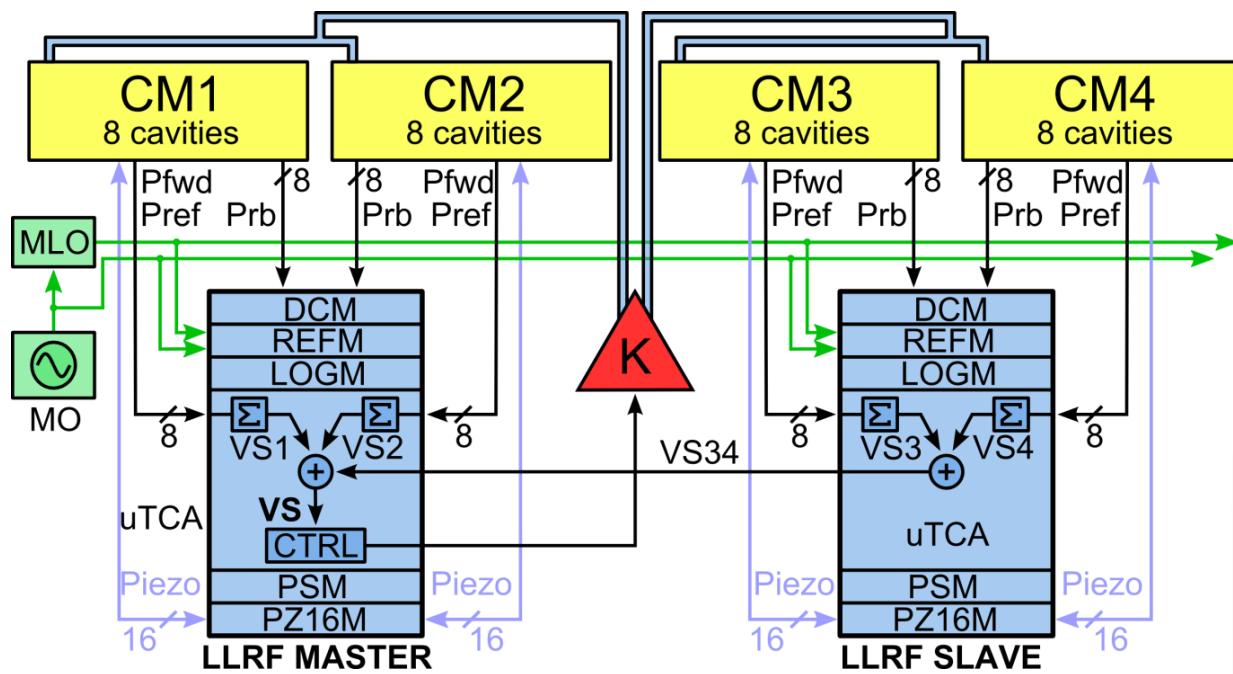
- 808 superconducting 1.3 GHz TESLA RF cavities
- 101 cryomodules (8 cavities)
- 25 RF stations (4 cryomodules)
- **1 LLRF system / RF station (i.e. per klystron)**



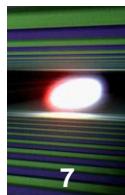
LLRF architecture for an RF station



RF station: semi-distributed LLRF system



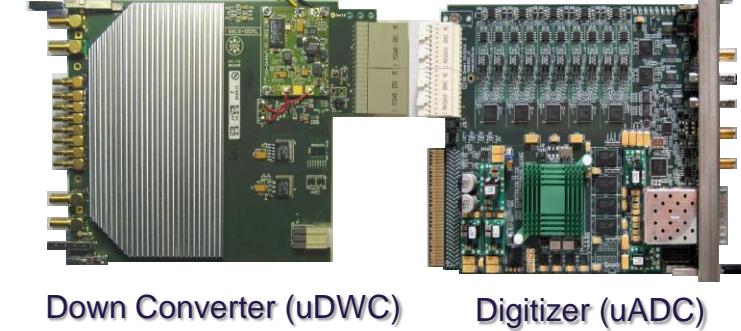
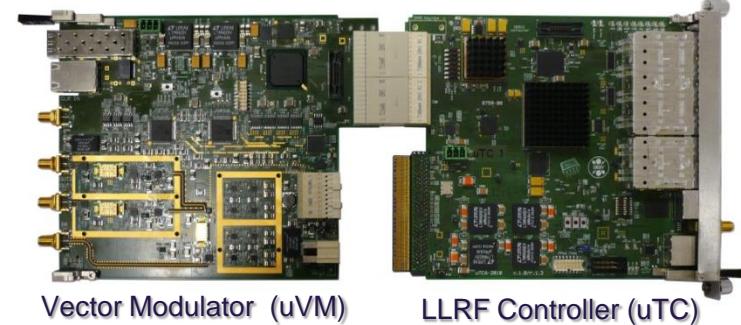
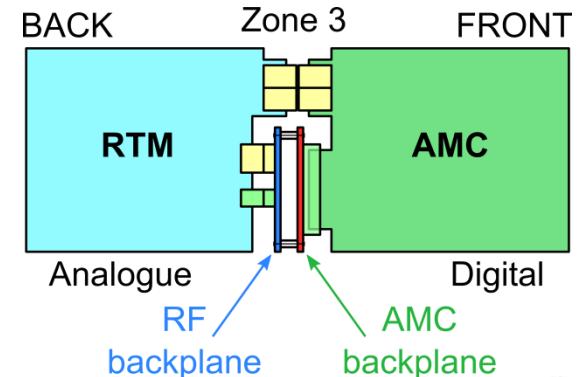
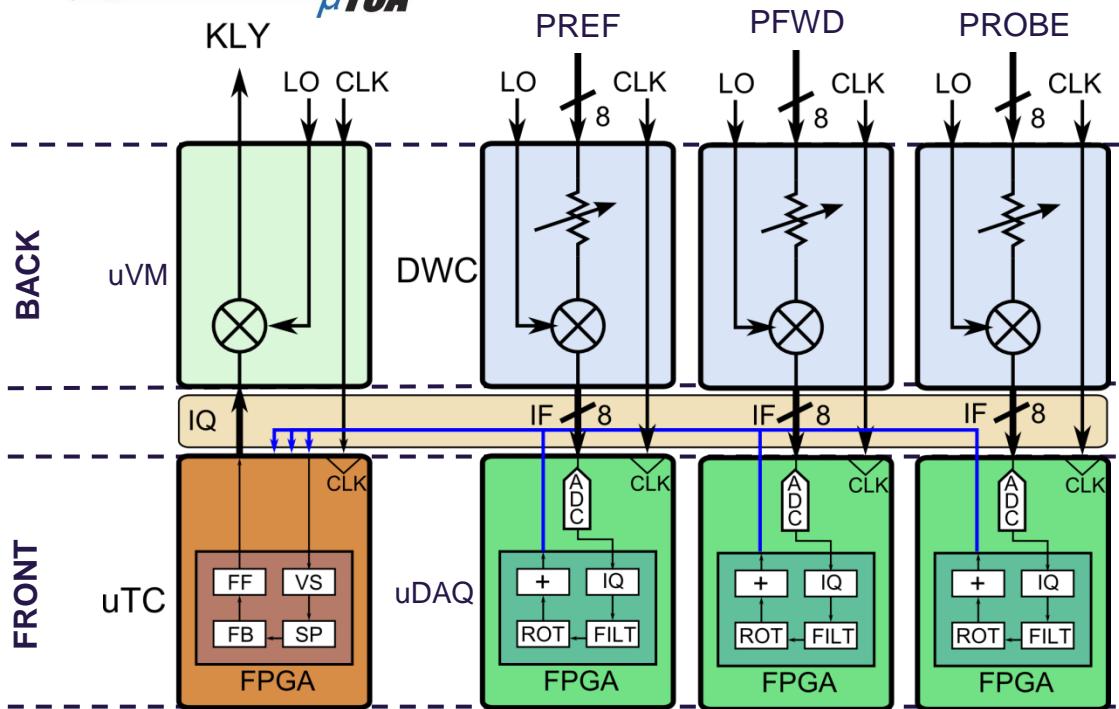
Section	Rack space	Redundancy
Gun, I1, 3H1	16U	Full
Linac 1	28U	Full
Linac 2	28U	No
Linac 3	28U	No



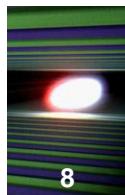
The MTCA.4 LLRF system



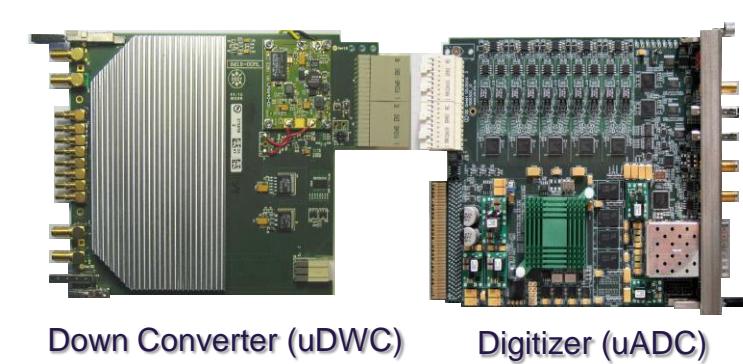
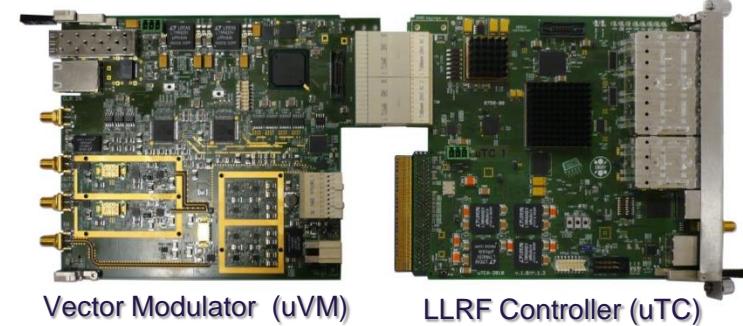
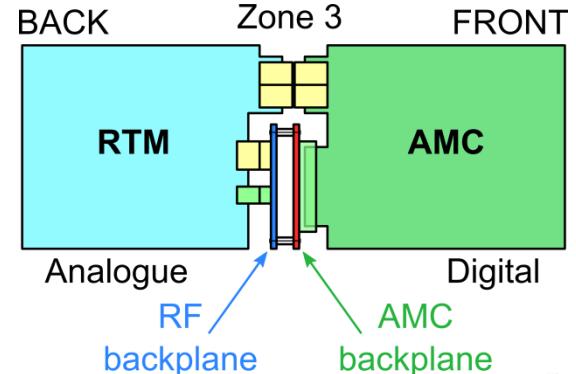
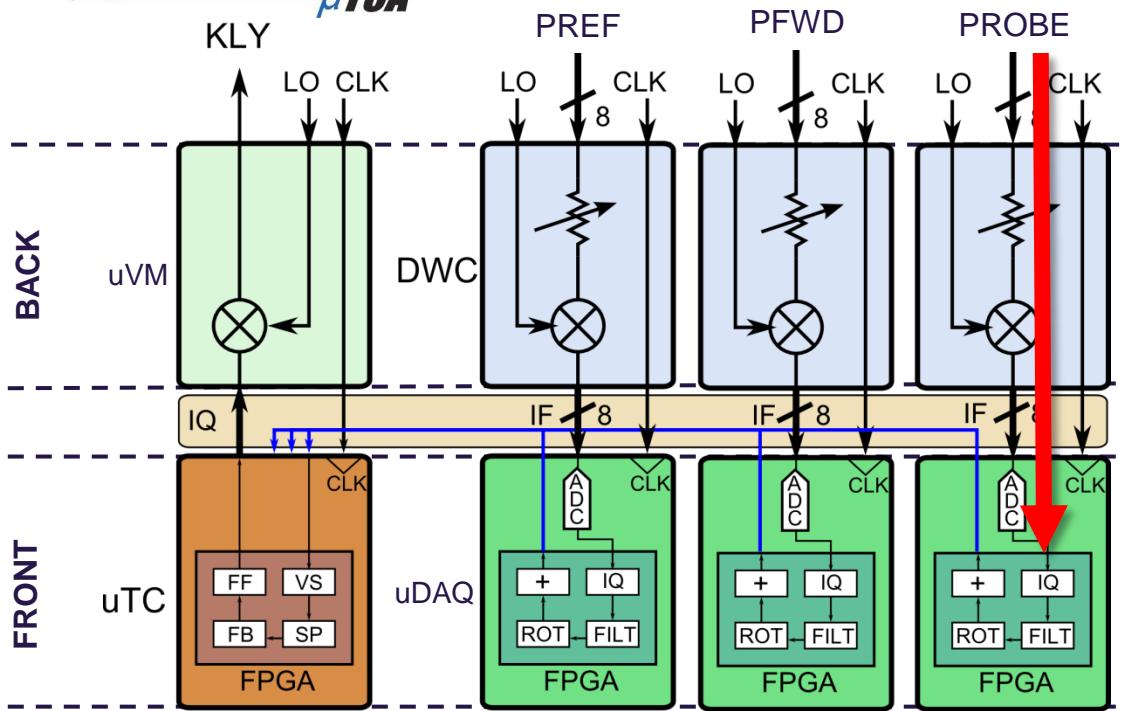
- AMC: Advanced Mezzanine Card
- RTM: Rear Transition Module
- 12 slots, hot swap
- Redundant power supply

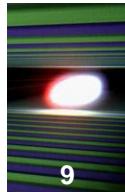


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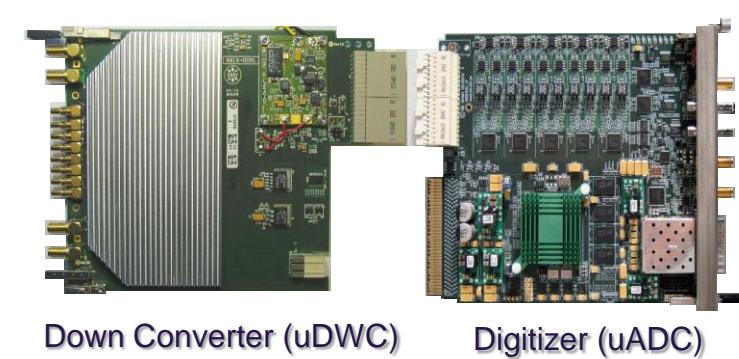
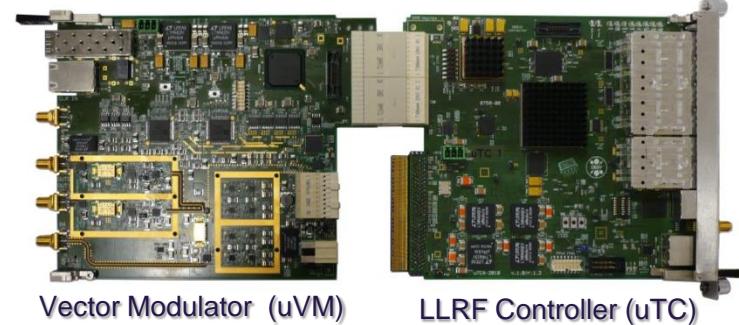
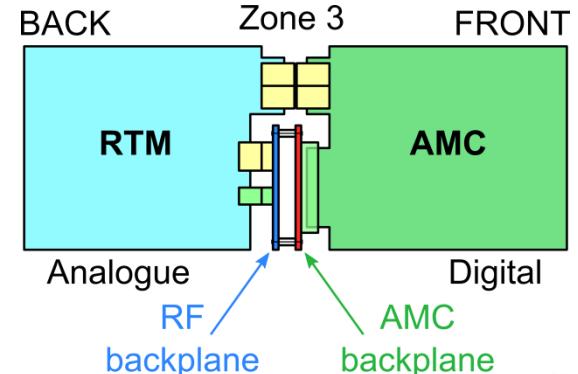
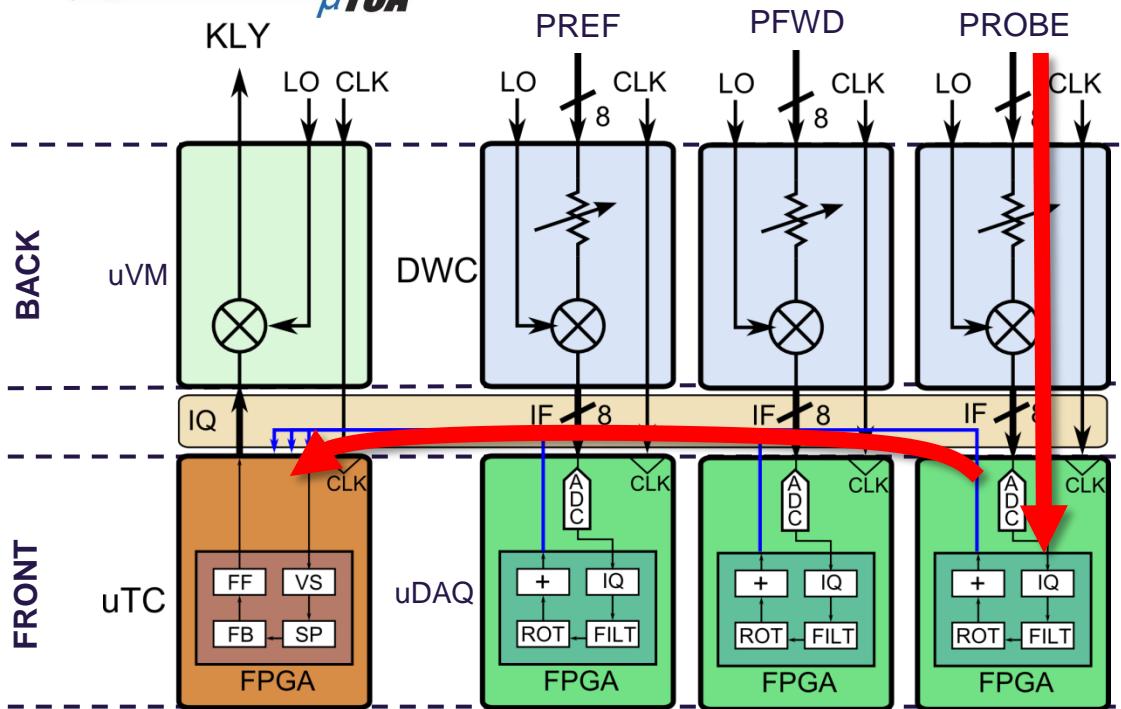




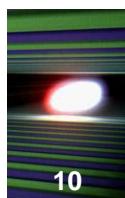
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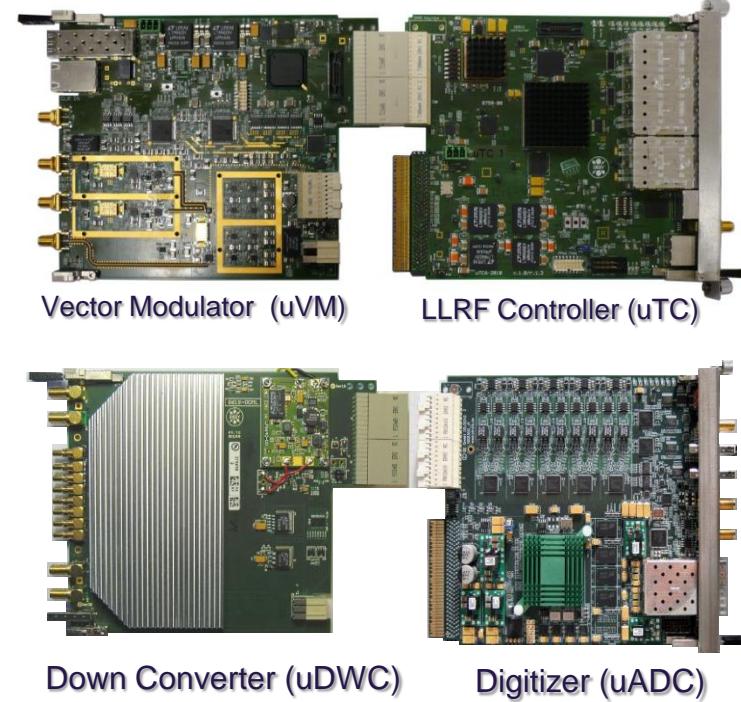
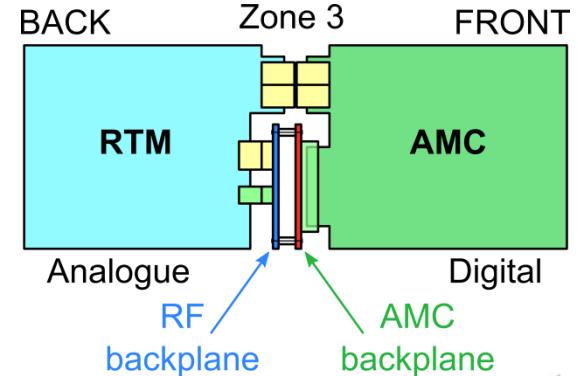
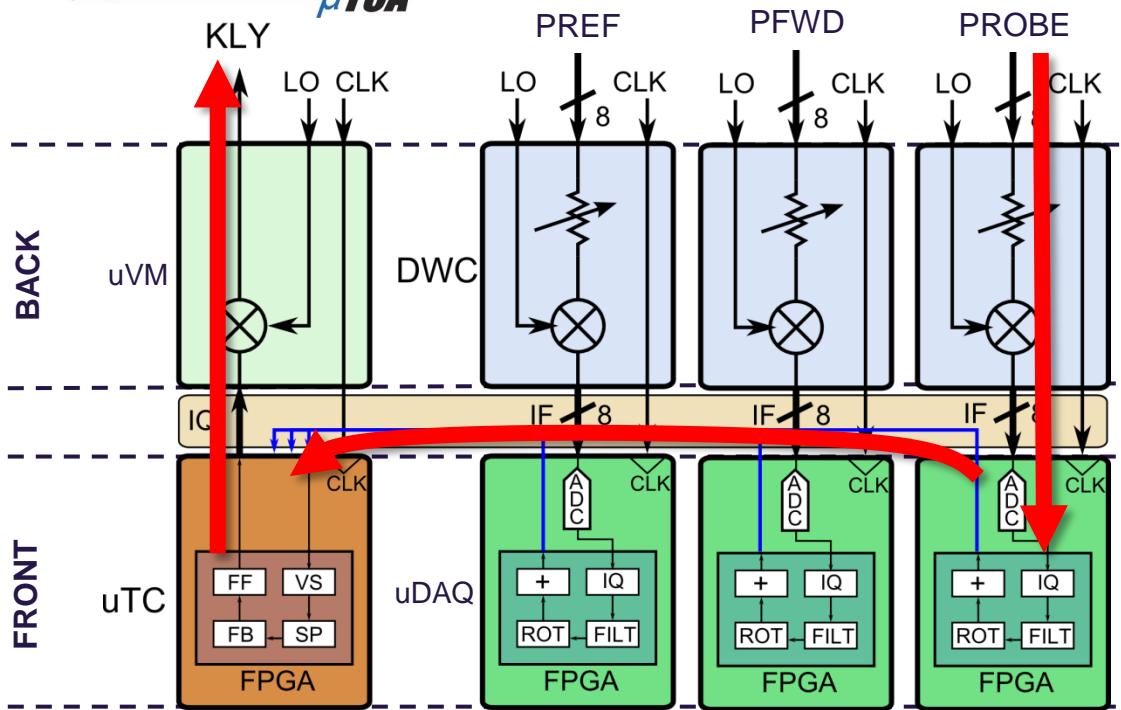
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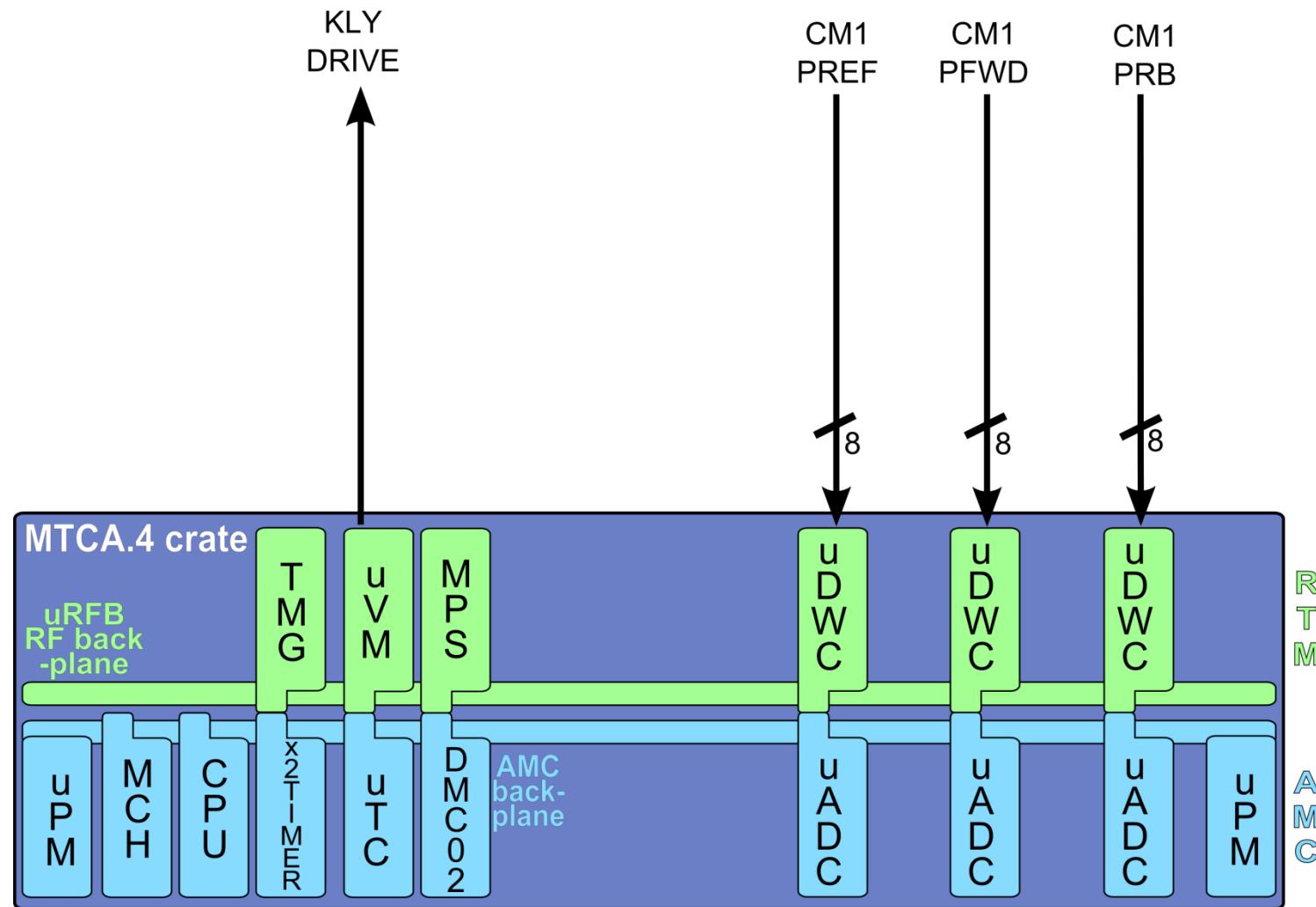
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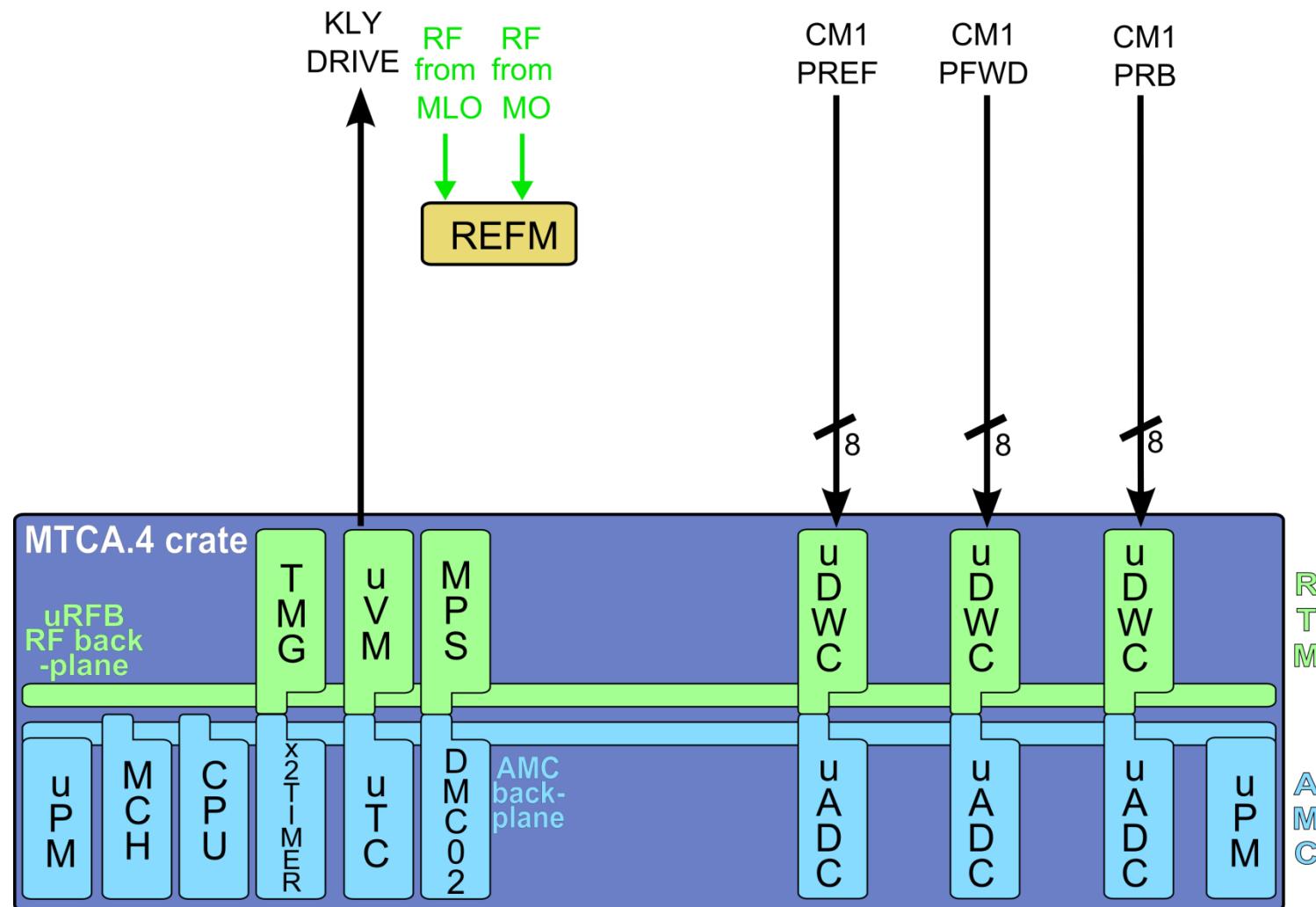
Overall LLRF System Description



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REFM

RF Reference Module



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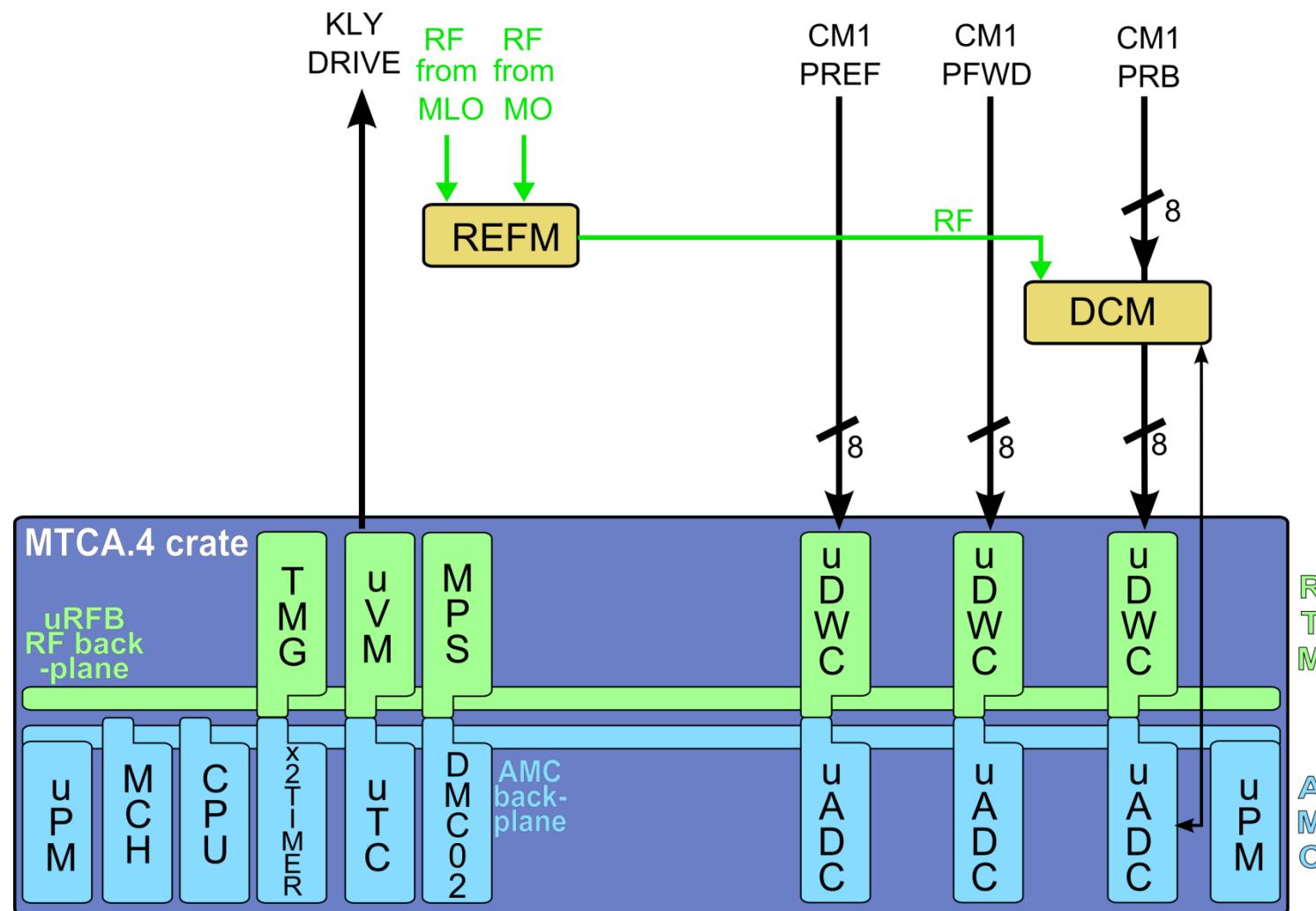
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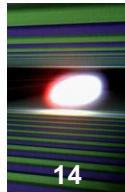
RF Reference Module

DCM

Drift Compensation

Module





Overall LLRF System Description

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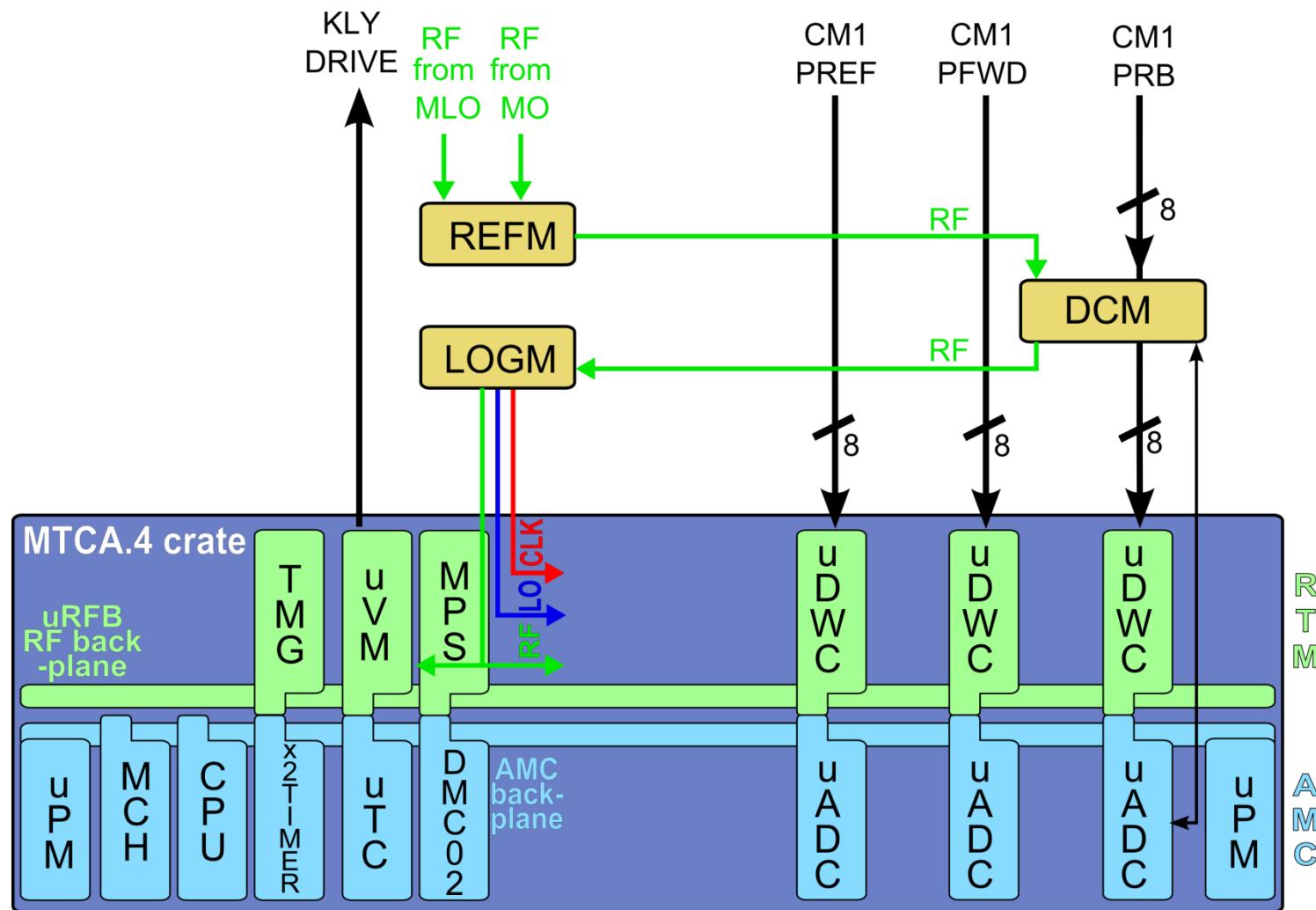
RF Reference Module

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Drift Compensation

Module

LOGM

Local Oscillator
Generation Module

Overall LLRF System Description

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DCM

Drift Compensation

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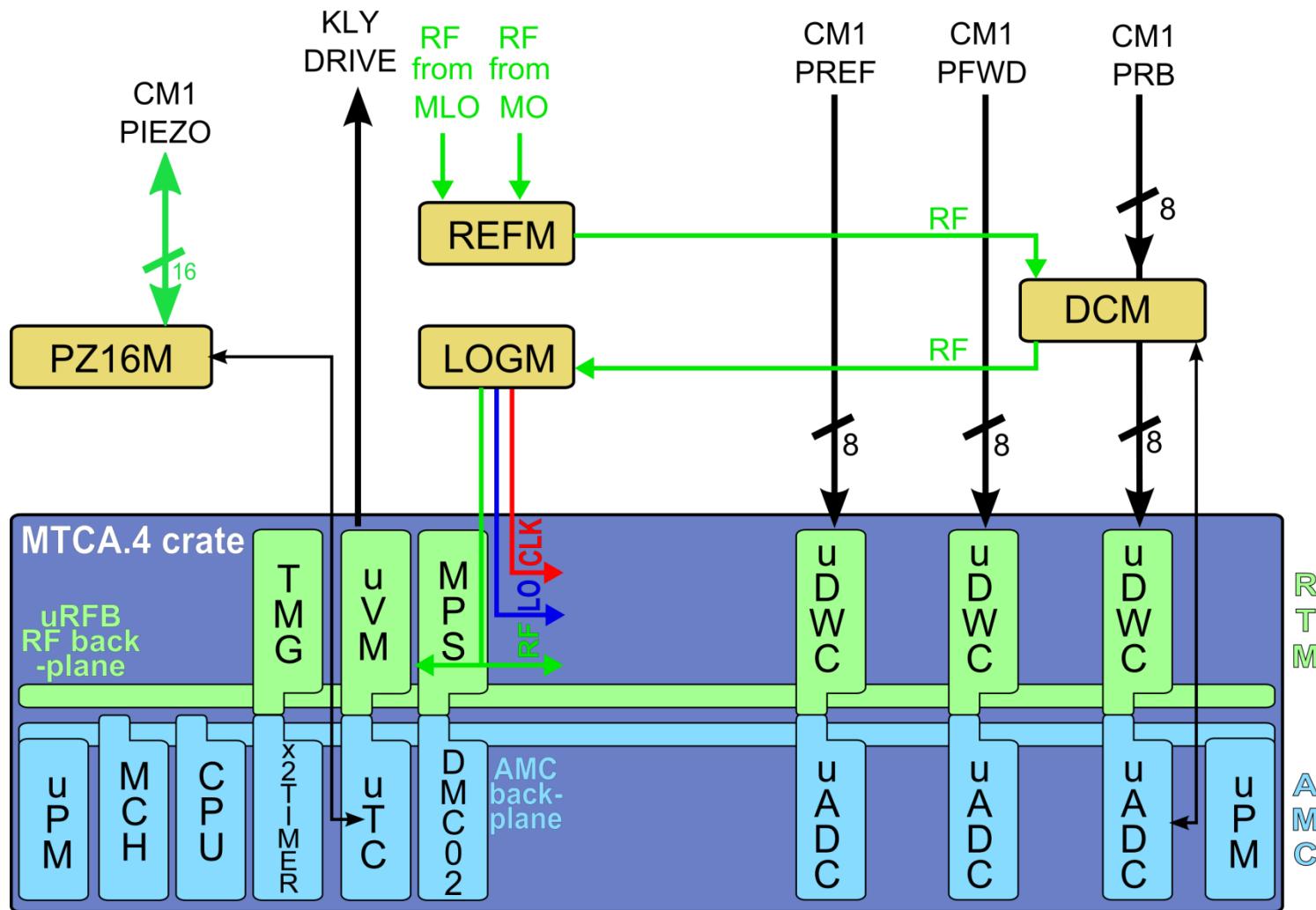
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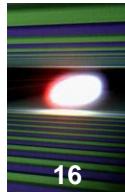
Local Oscillator

Generation Module

PZ16M

Piezo Driver Module





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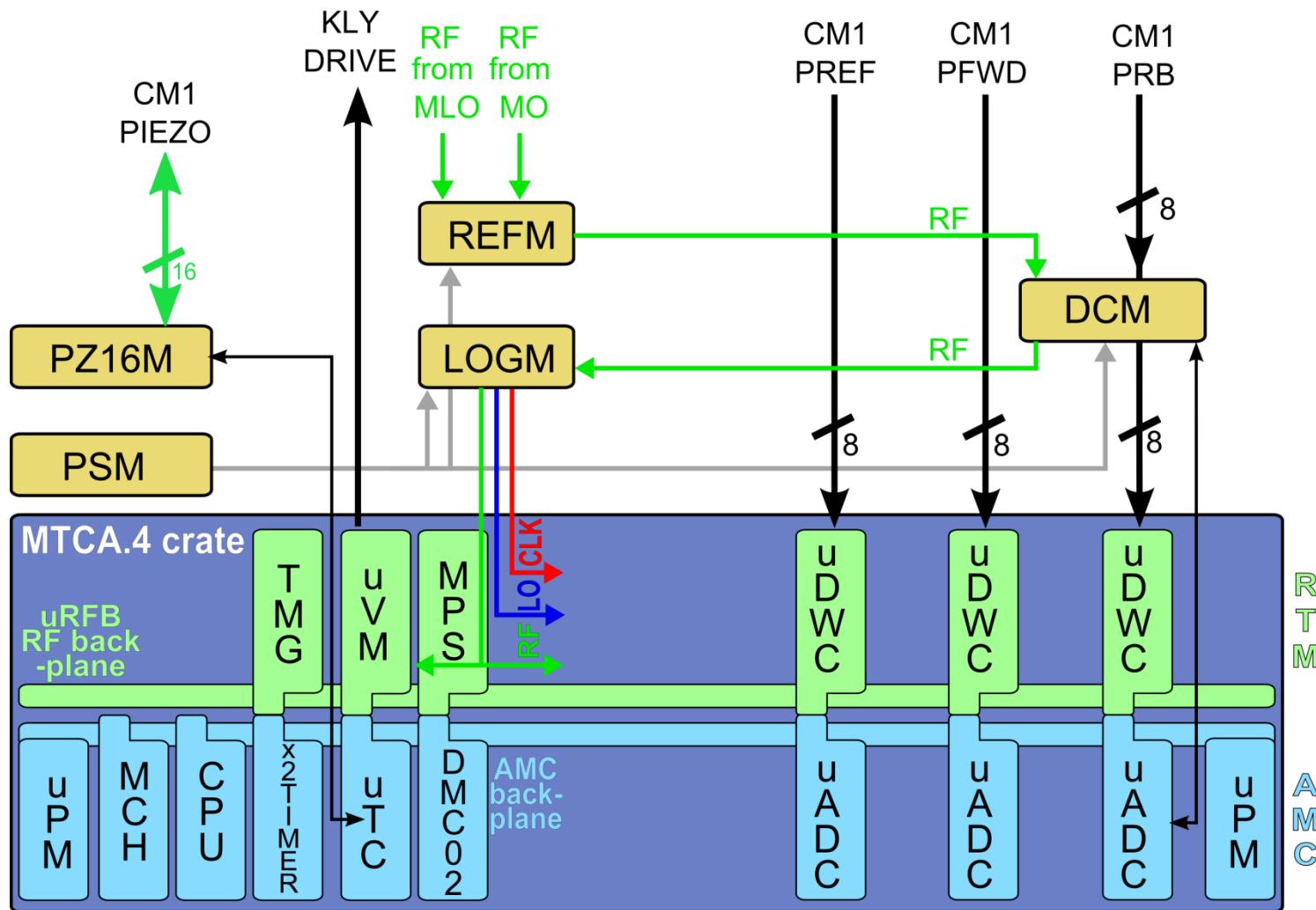
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Piezo Driver Module

PSM

Power Supply Module



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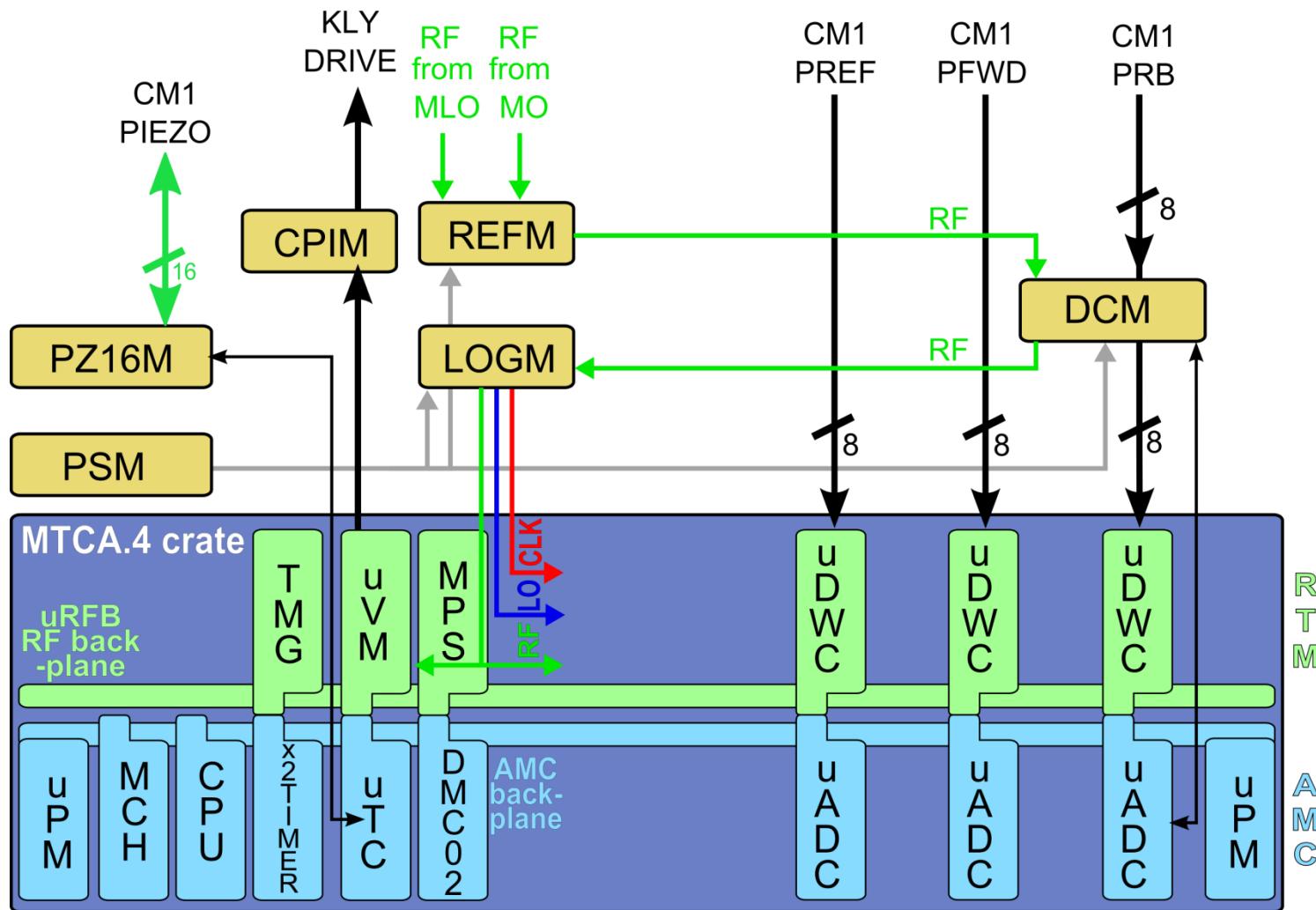
PZ16M

Piezo Driver Module

PSM

Power Supply Module

CPIM

Coupler Processing
interlock Module

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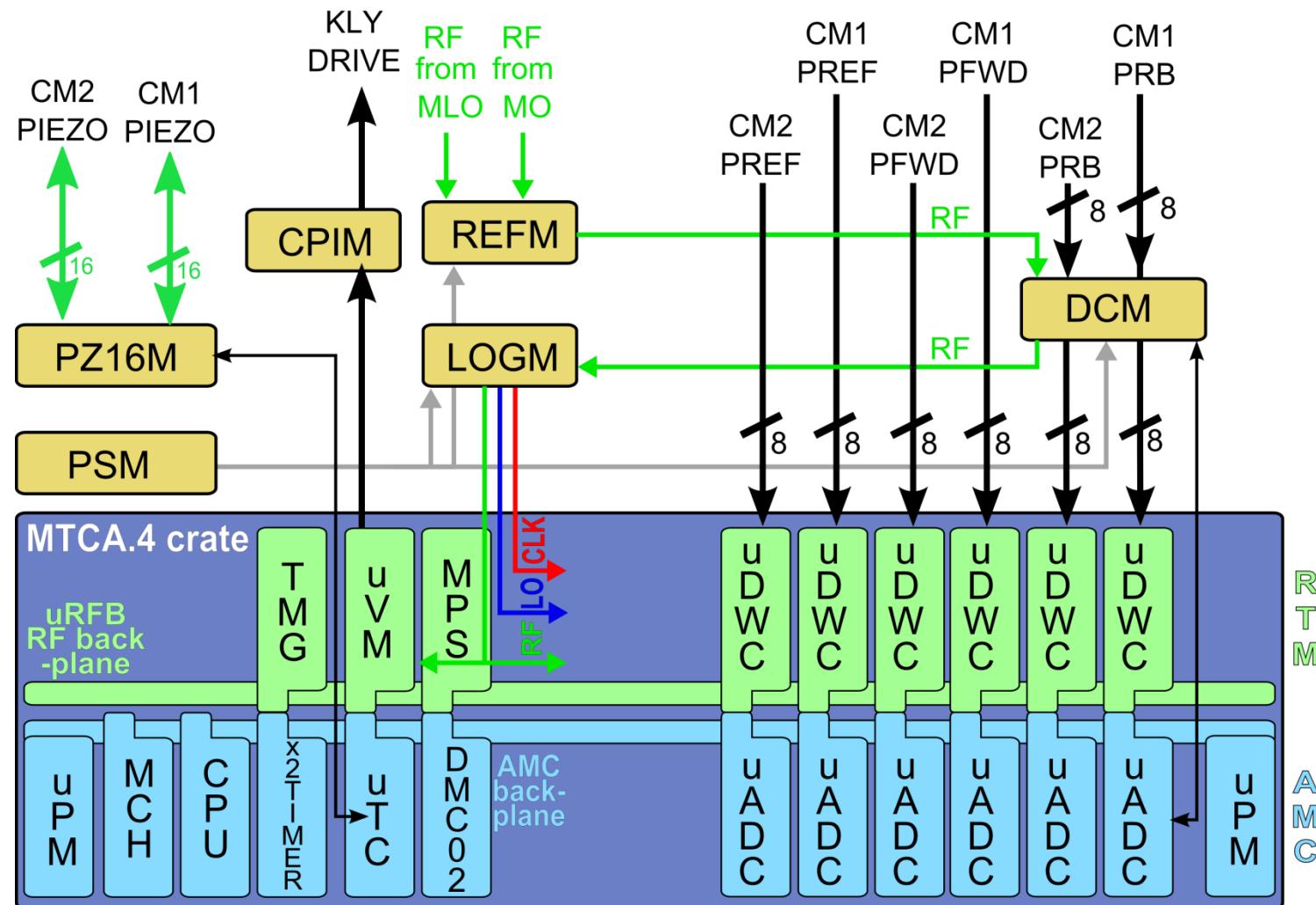
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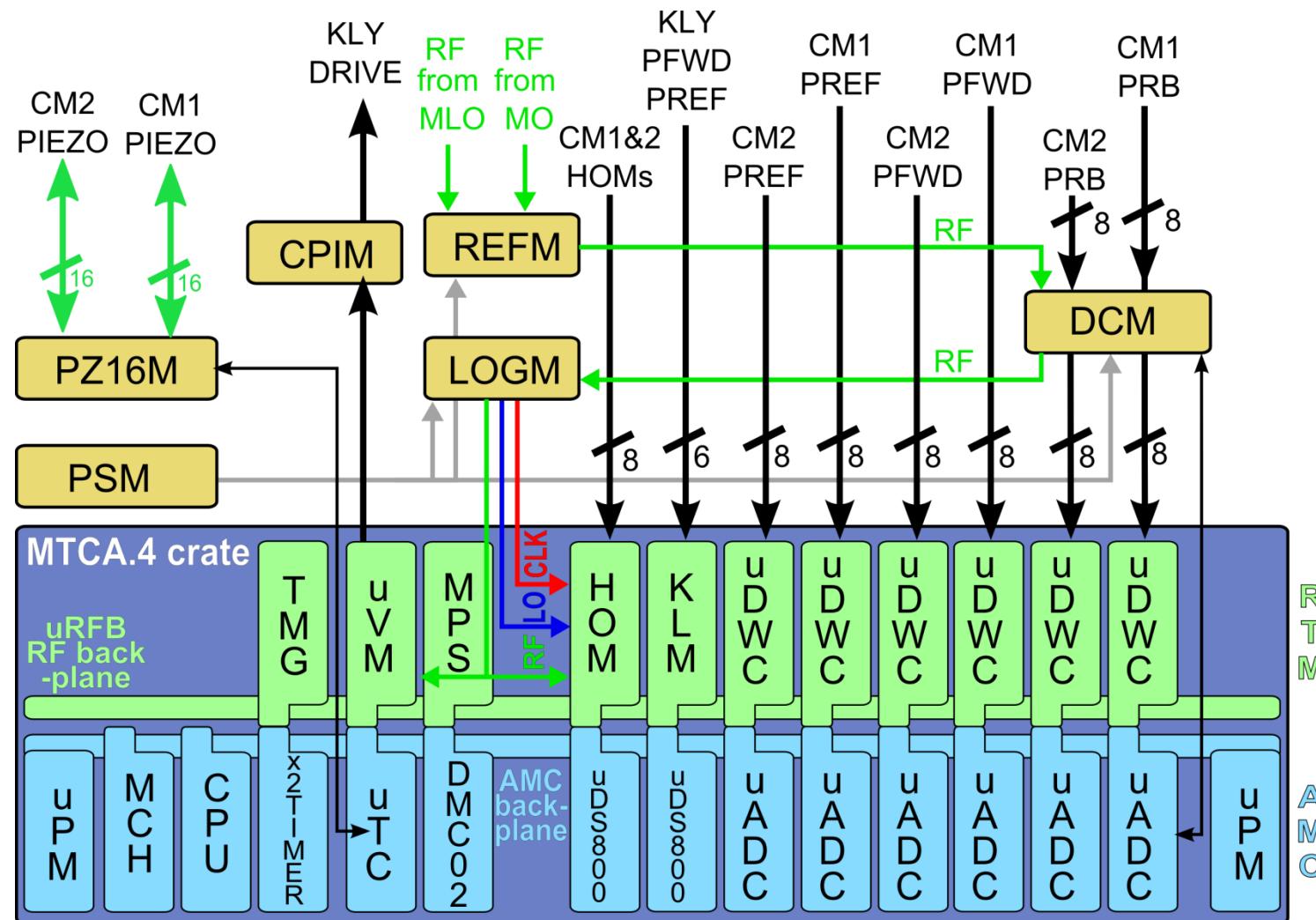
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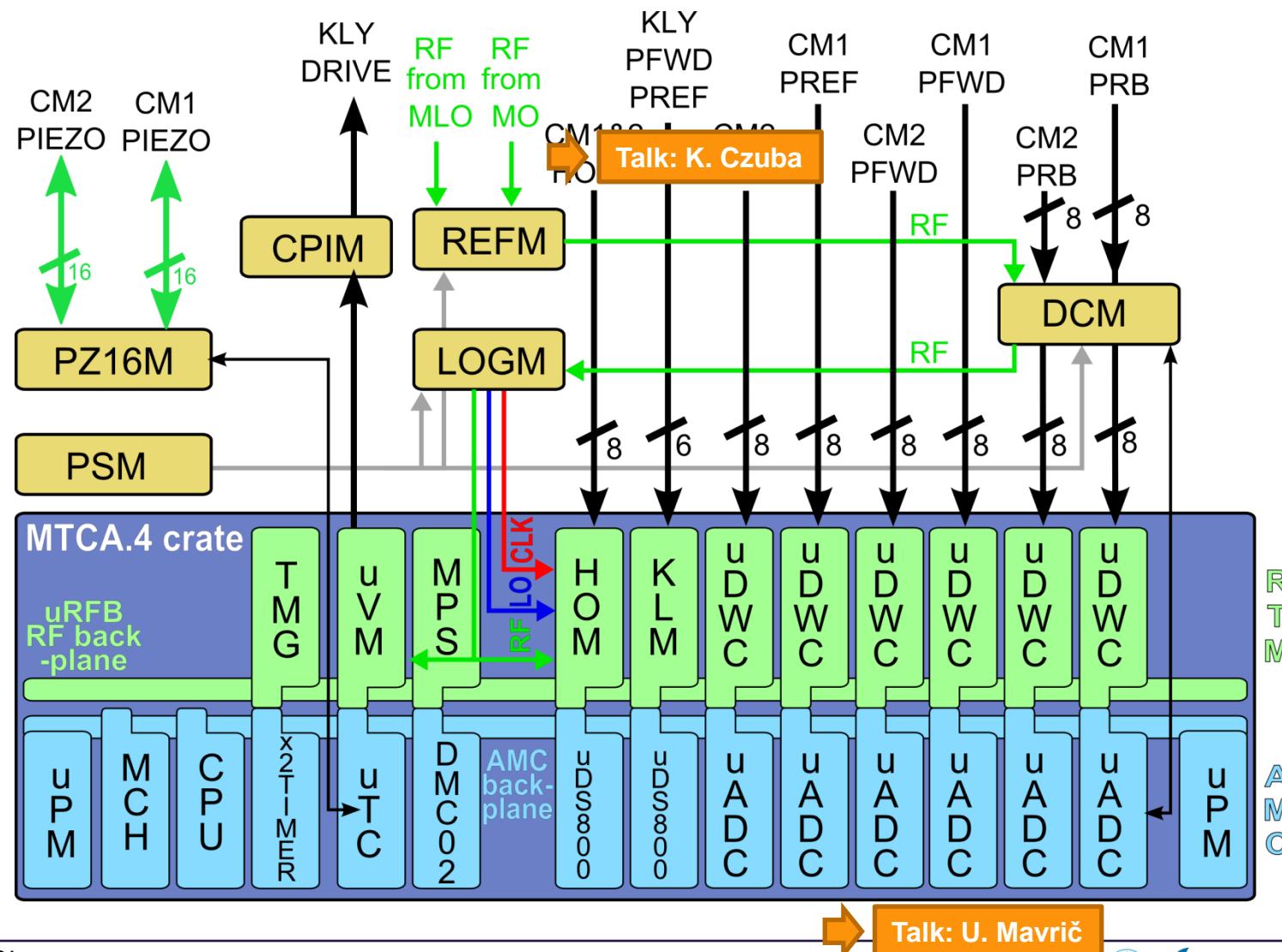
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HIGHLIGHTS

LLRF System:

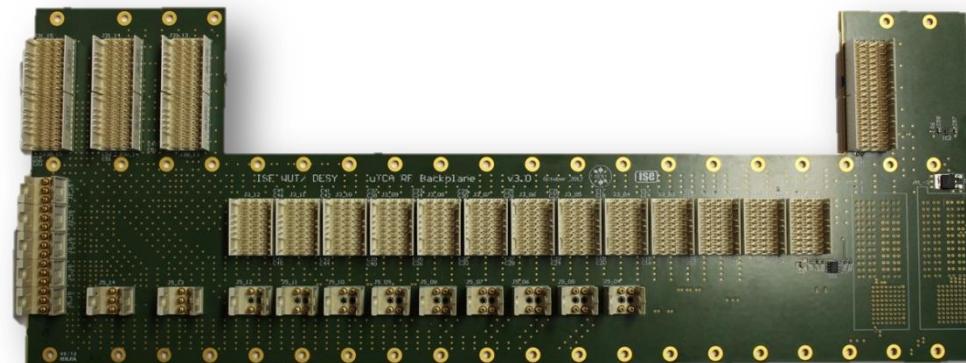
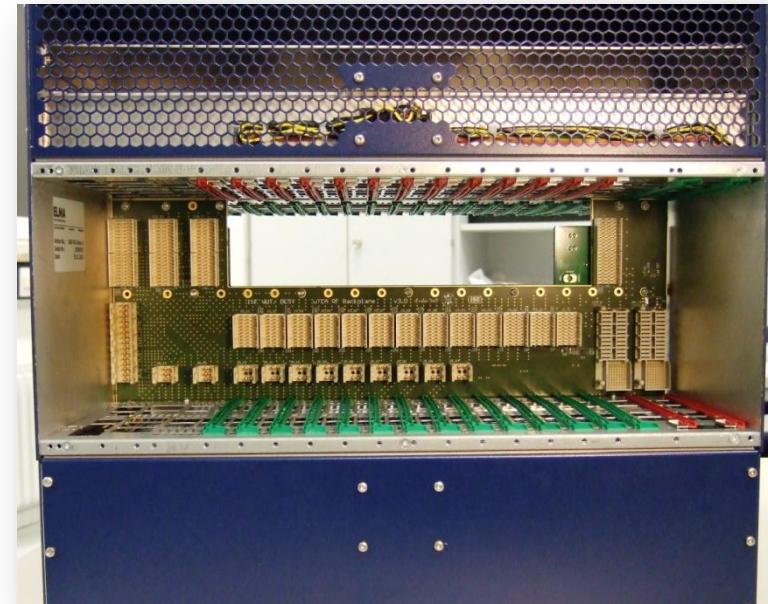
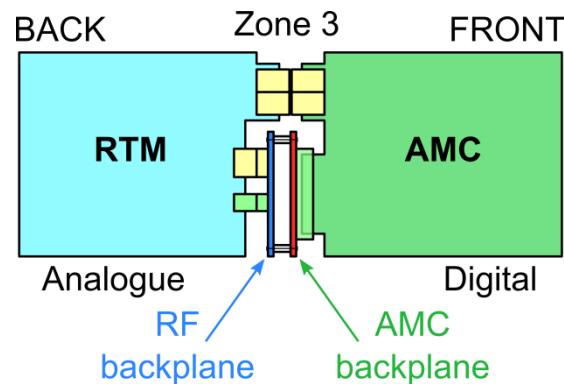
A few highlights

XFEL LLRF in a Table ...

19" CHASSIS / INFRASTRUCTURE	MTCA.4 MODULE	SOFTWARE	FIRMWARE
PSM	MTCA.4 9U crate	LLRF controller	IQ detection
REFM	MTCA.4 2U crate	FSM	AP calculation
REFM-Interferometer	MTCA.4 uRFB	BLC	Cavity limiters and pre-limiters
REFM-L2RF	MTCA.4 uPM	LFF	$8\pi/9$ IIR filters
PZ16M	MTCA.4 uMCH	BBF	MIMO 4 th order
LOGM	MTCA.4 uCPU	DAQ	Channel input delay
CPIM	MTCA.4 x2timer	Diagnostics server	Output rotation and delay
MO	MTCA.4 uADC	Klystron linearization	Virtual probes
MO-distribution	MTCA.4 uTC	Auto Q _L	Online detuning – Q _L
TMCB	MTCA.4 uDS800	Auto Piezo	Channel calibration
Piezo discharge	MTCA.4 uLOG_carrier	Auto tuner	Programmable attenuators
Reference line	MTCA.4 uLOG_RF	Quench detect	KLM
Interferometer	MTCA.4 DAMC02	Energy server	DCM control and communication
Injector racks / shielding	MTCA.4 RadMon	Q _L scan	PZ16M control and communication
Temp. controlled Racks	MTCA.4 uDWC (1.3GHz & 3.9GHz)	Piezo scan	Cavity simulation
Patch panels	MTCA.4 uVM (1.3GHz & 3.9GHz)	Tuner scan	Board & peripherals (Intern. Interface)
Outer rack cabling	MTCA.4 uKLM-RTM	Cavity modes identification	LLL
Inner rack cabling	MTCA.4 uHOM-RTM	KLM	CLK
Klystron test stand	MTCA.4 uDWC-VM	Waveguide pressure control	DAQ
MTCA.4 board tests setups	MTCA.4 eMCH	MO diagnostics	DAC
Chassis & crate system test setups	MTCA.4 ePM	Performance statistics	PCIe

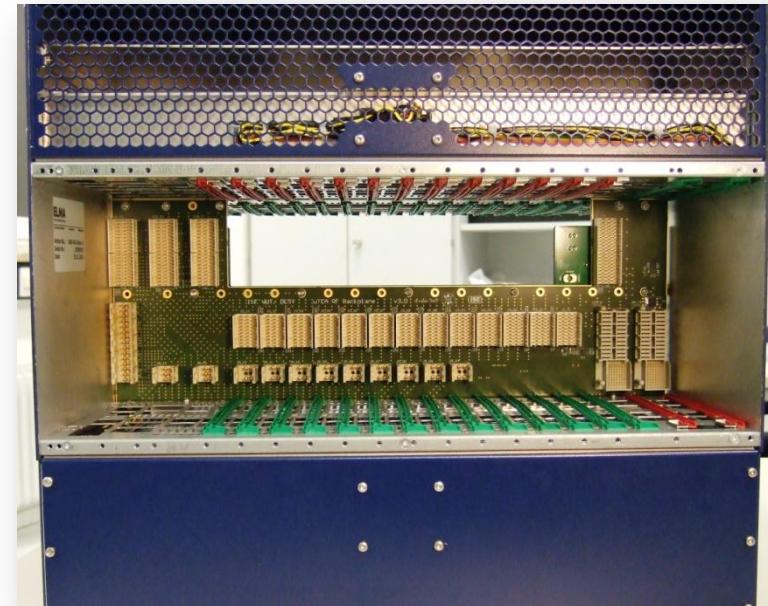
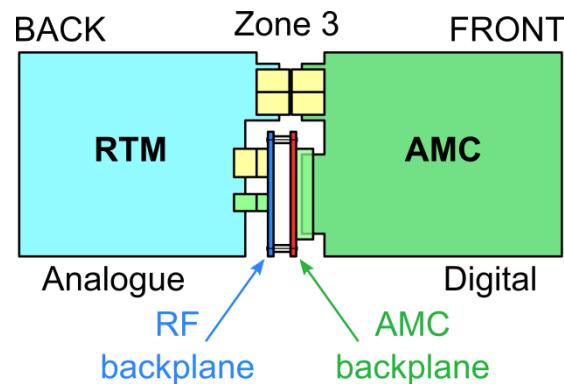
HARDWARE HIGHLIGHTS

■ RF backplane: uRFB

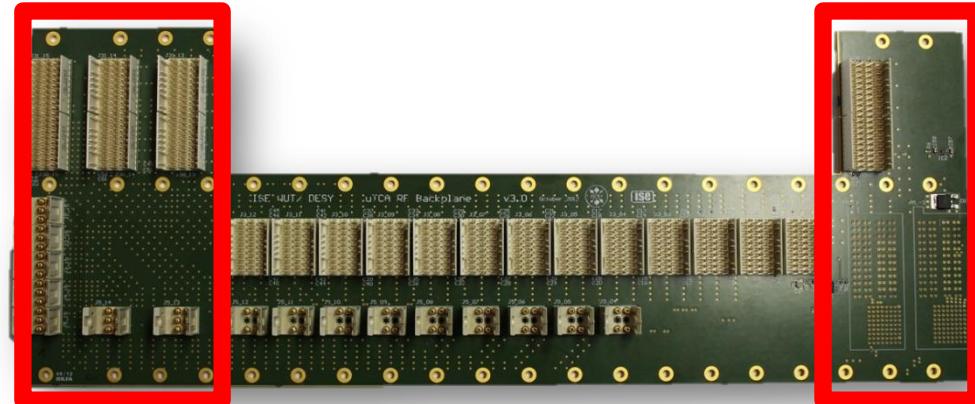


HARDWARE HIGHLIGHTS

■ RF backplane: uRFB

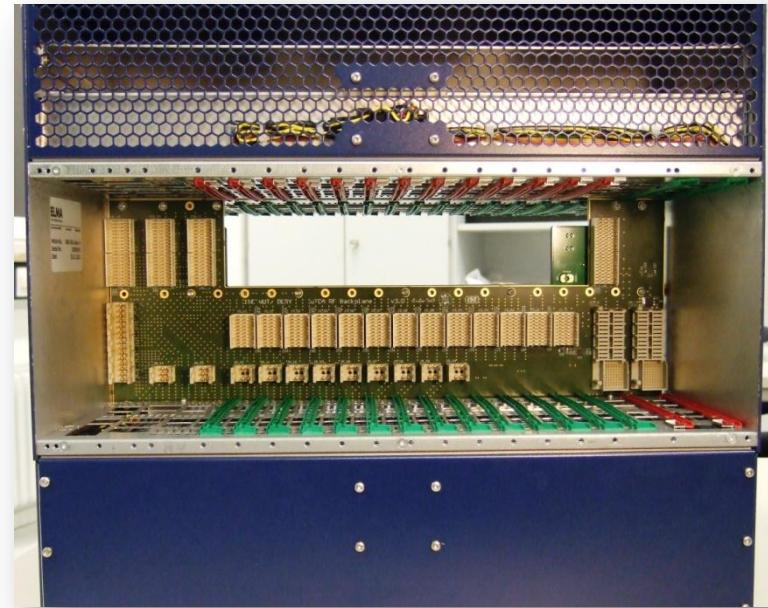
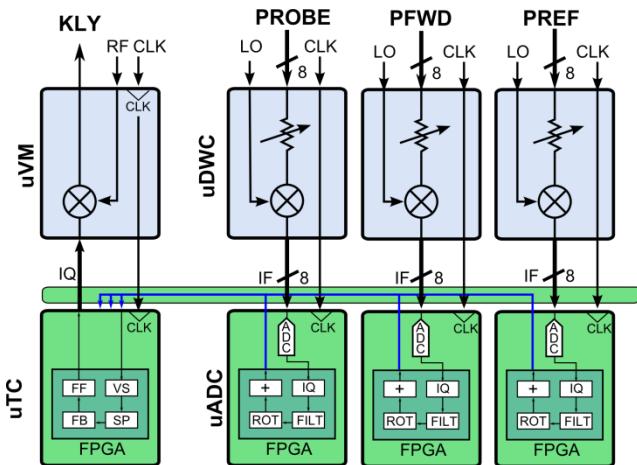
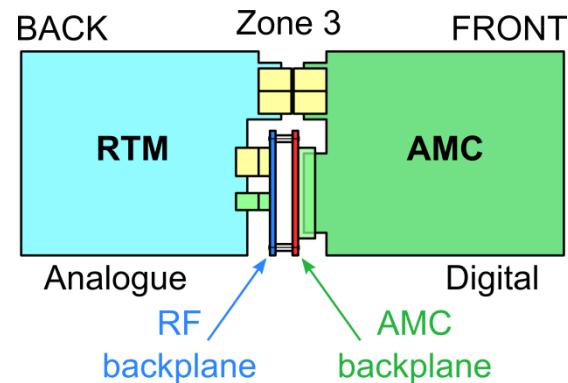


■ Extended rear module: eRTM

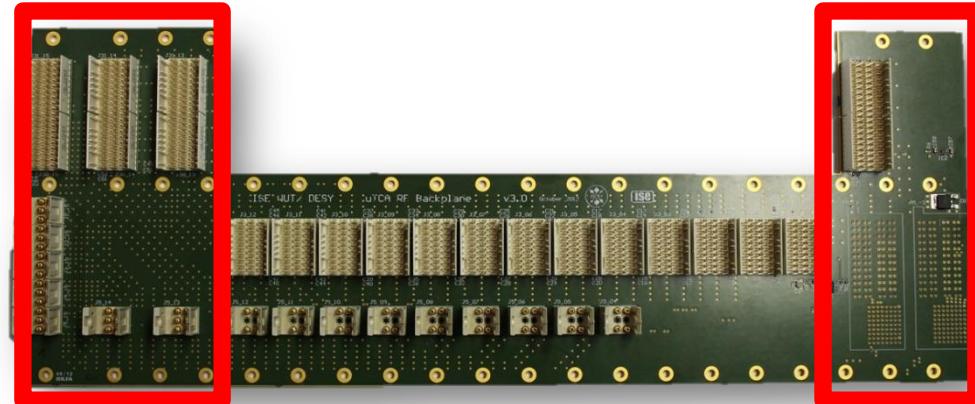


HARDWARE HIGHLIGHTS

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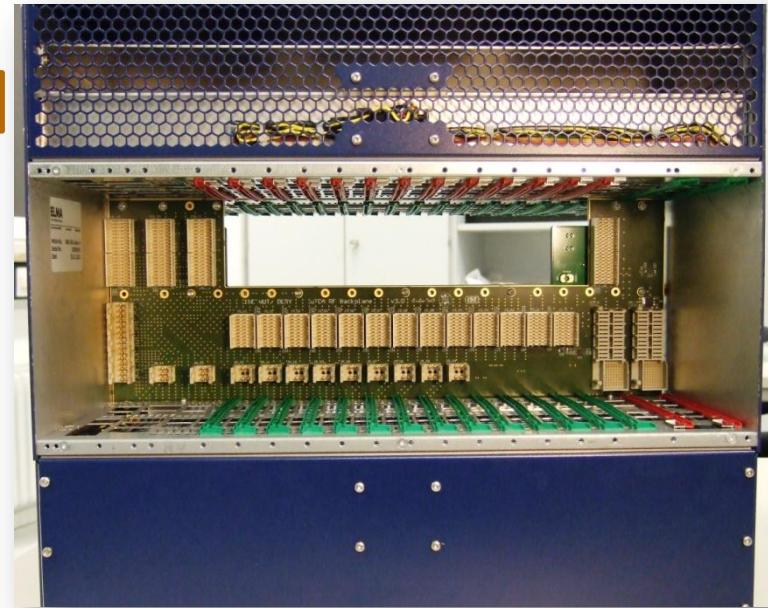
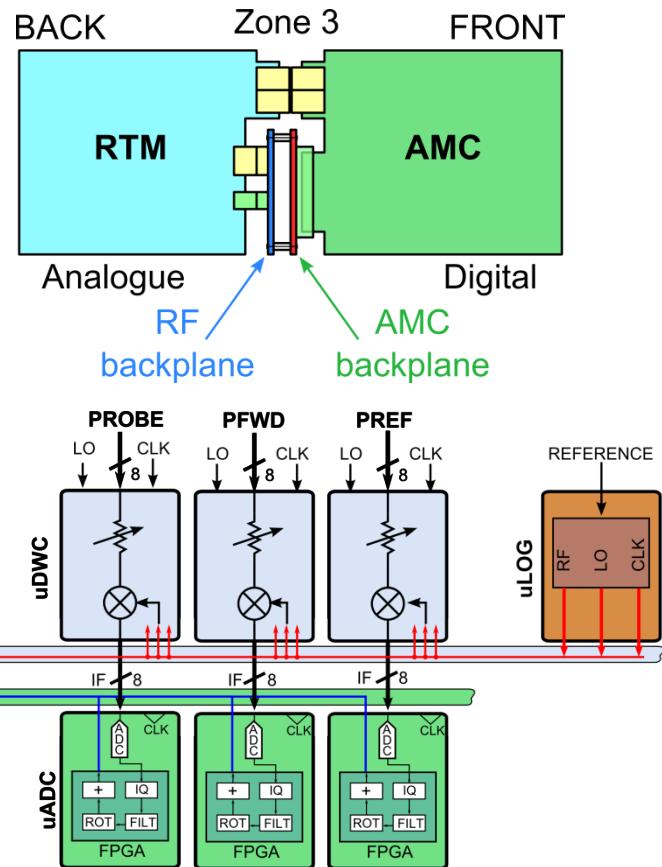


■ Extended rear module: eRTM

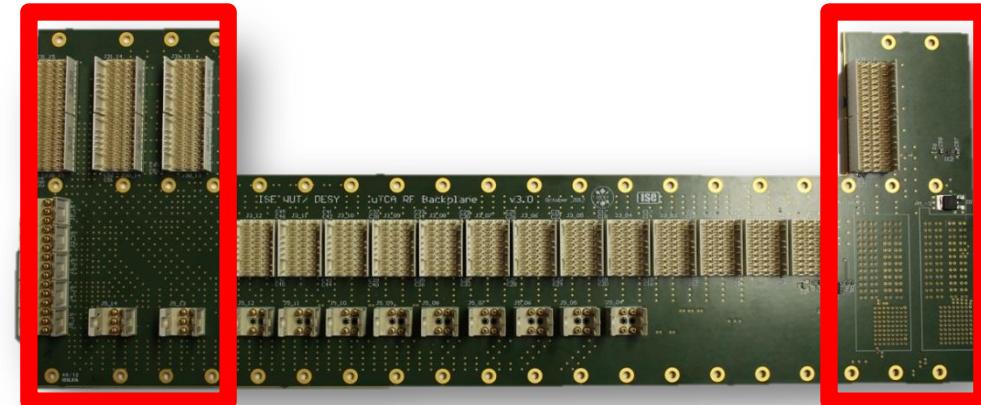


HARDWARE HIGHLIGHTS

■ RF backplane: uRFB ➔ Talk: K. Czuba



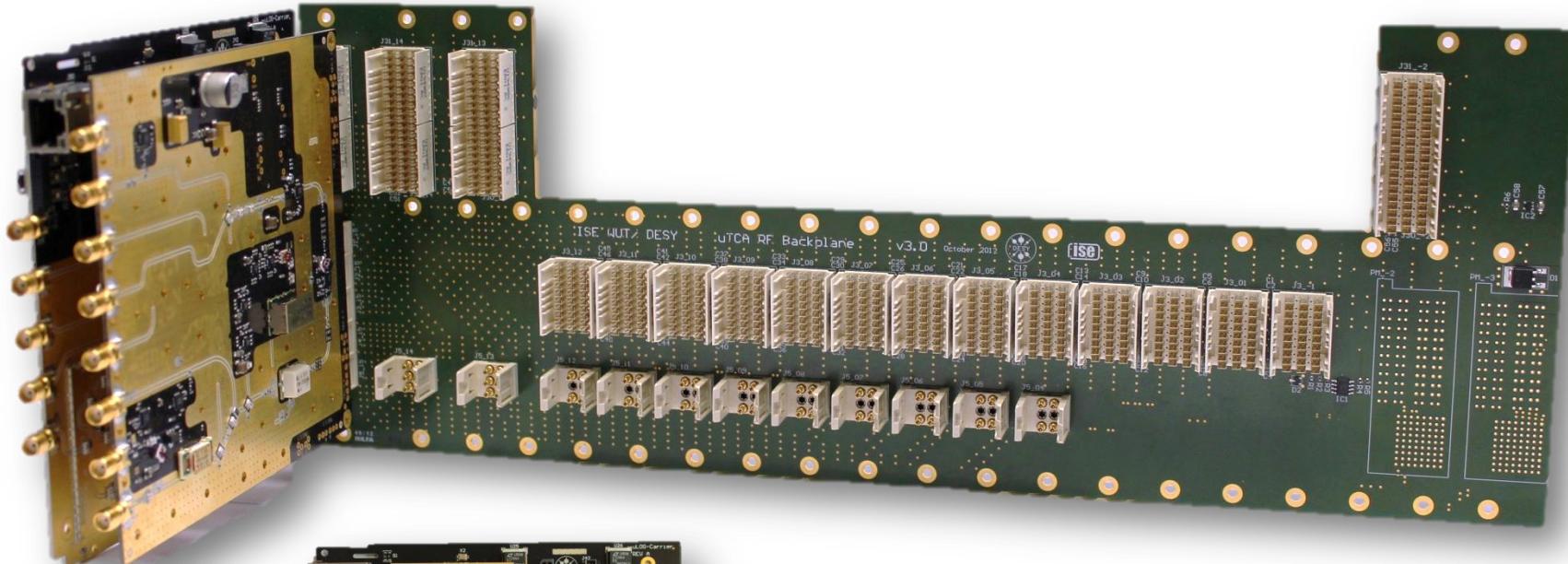
■ Extended rear module: eRTM



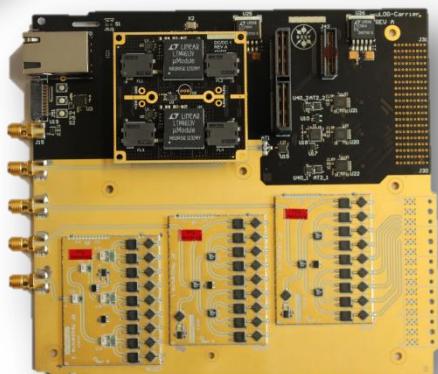
HARDWARE HIGHLIGHTS

■ uRFB with uLOG

→ Poster: T. Rohlev

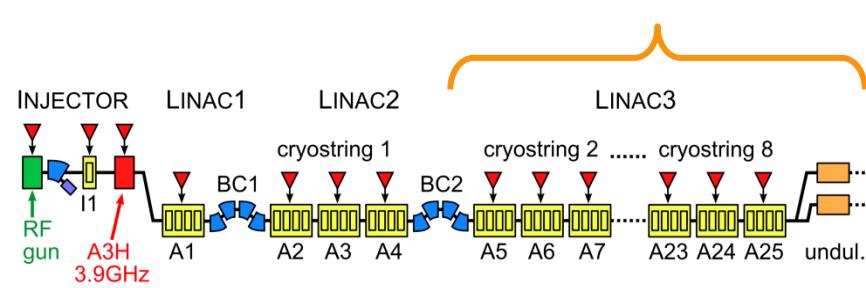


uLOG RF carrier



Courtesy U. Mavrič , T. Rohlev

L3 = 21 RF stations
→ 42 crates equipped with uRFB + uLOG



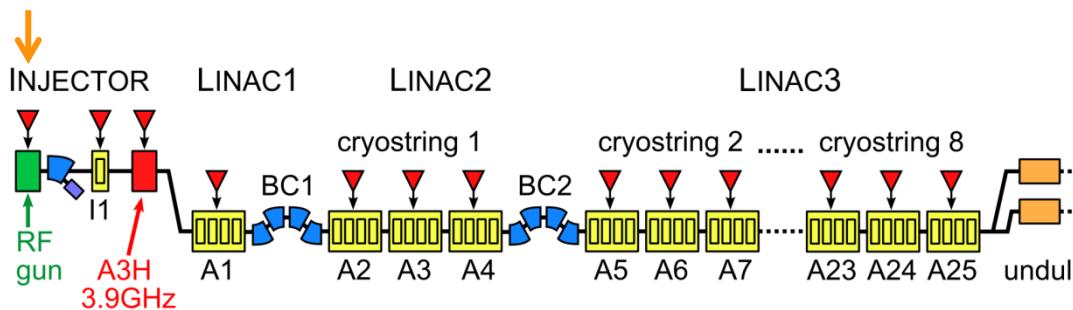
HARDWARE HIGHLIGHTS

- 8 ch. down-converter 1 ch. up-converter
uDWC8-VM1

→ Poster: M. Hoffmann



RF gun control

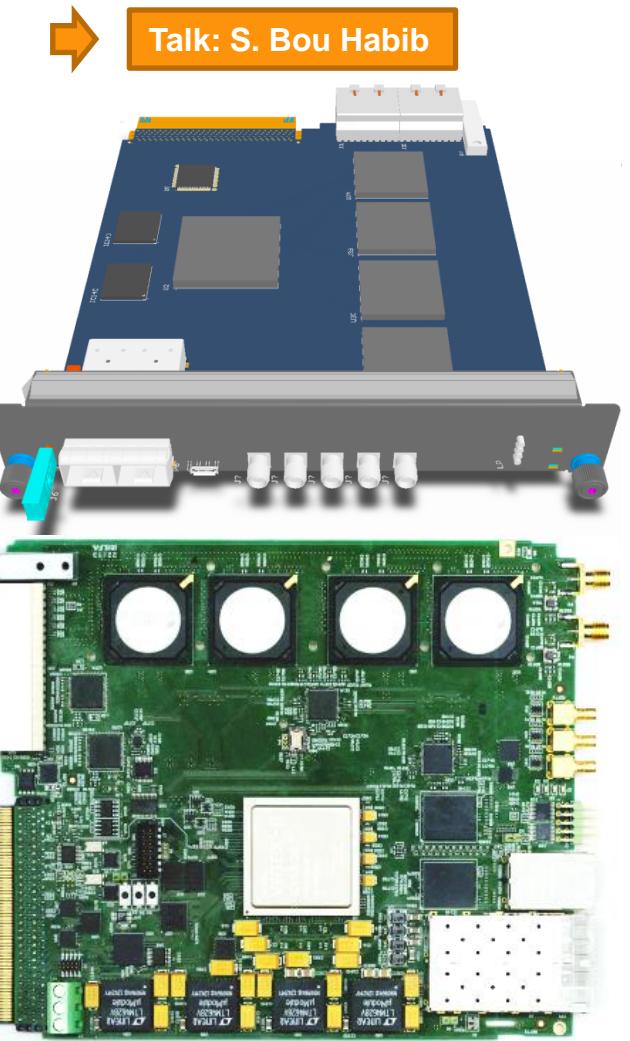
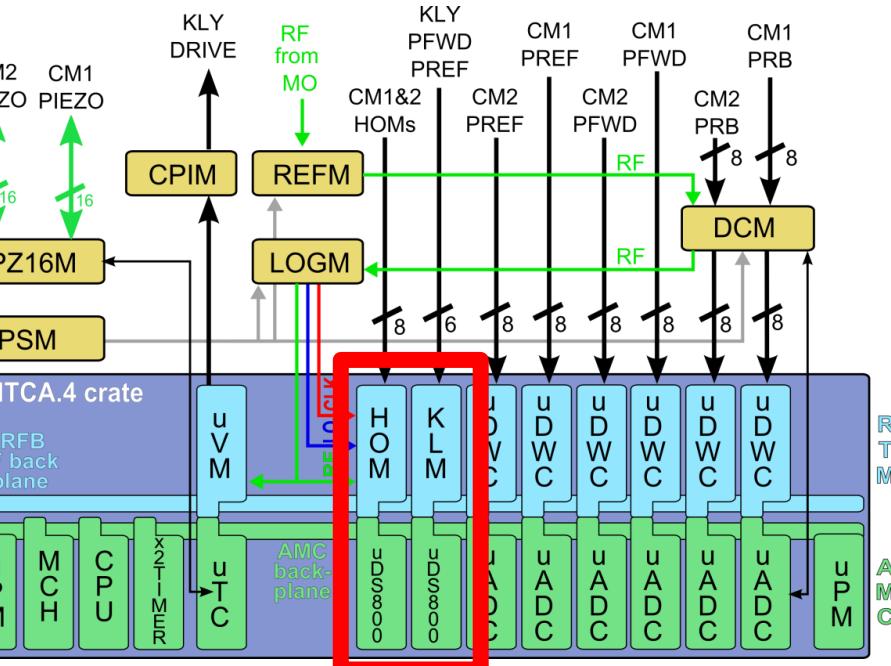


Courtesy M. Hoffmann

HARDWARE HIGHLIGHTS

■ Direct sampling fast digitizer: **uDS800**

- AMC
- 8x channels
- 800 MSPS
- Possible applications (HOMs, KLM)

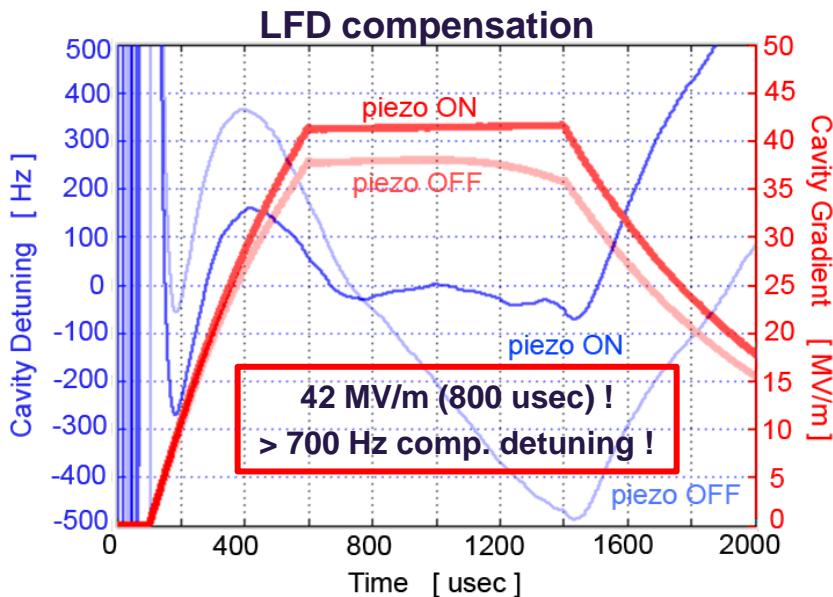


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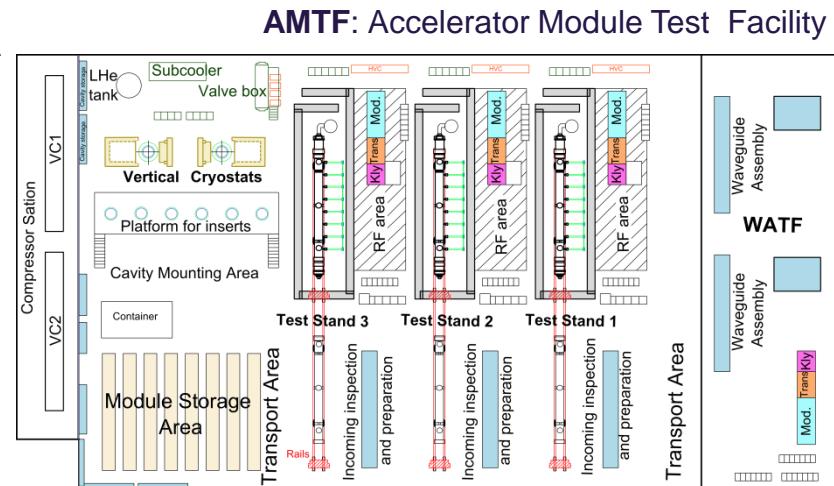


OPERATION HIGHLIGHTS

■ XFEL cryomodule LLRF tests*



- RF TESTS**
- Cavity fundamental mode spectra measurements
 - Higher order modes (HOM) couplers spectra
 - Calibration of cold RF cables
 - Warm coupler conditioning
 - Coupler conditioning during cool-down
 - Loaded Q measurements and adjustment
 - Cavity gradient calibration
 - Cavity gradient performance measurement
 - Cryomodule dynamic heat-load measurement



→ **Poster : K. Przygoda**

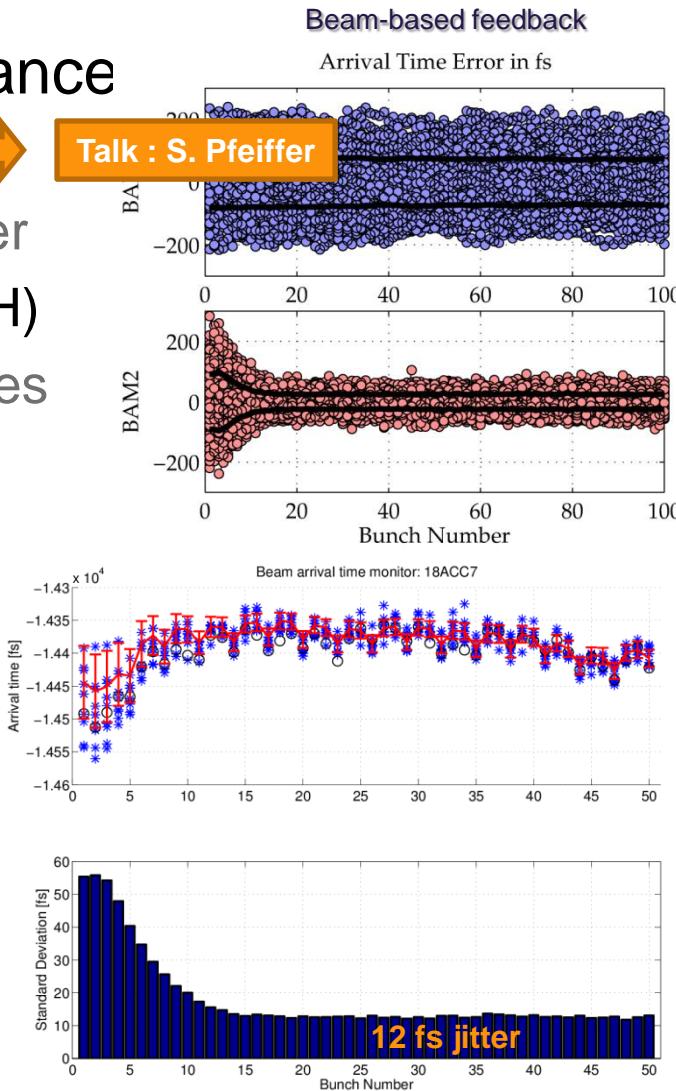
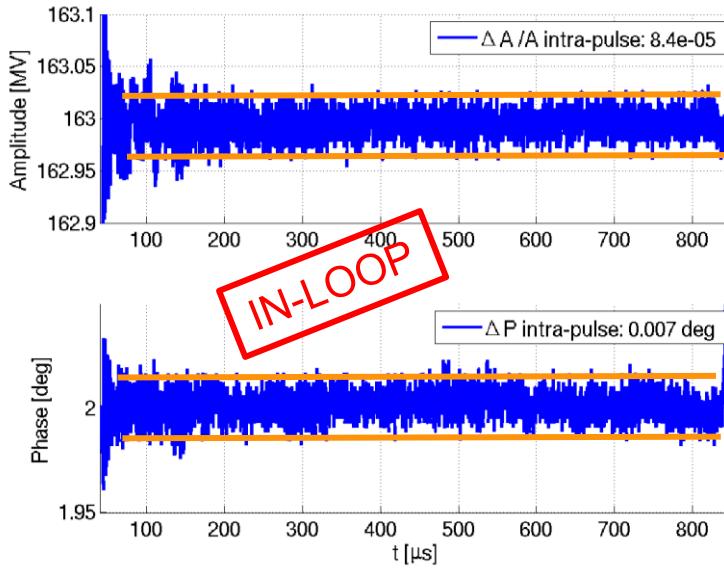
LLRF TESTS

- Piezo capacitance measurement (before and during cooldown)
- Microphonics measurement
- Piezo scans (cavity detuning transfer function)
- $8\pi/9$ and $7\pi/9$ mode identification
- Cavity tuner motor characterization
- Cavity loaded Q motor characterization
- Lorentz force detuning compensation
- Closed-loop nominal gradient operation.

OPERATION HIGHLIGHTS

■ Improvement of LLRF control performance

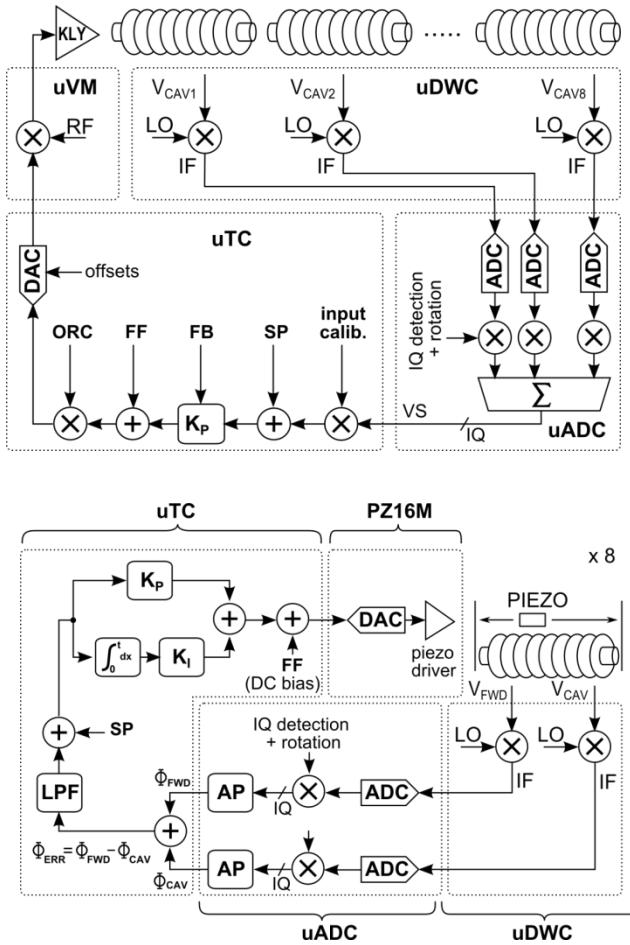
- Beam-based feedback (tested at FLASH) → factor of 3 improvement in arrival time jitter
- Cavity fundamental modes (tested at FLASH)
→ detection & filtering of $8\pi/9$ and $7\pi/9$ modes
- $\Delta A/A < 9 \times 10^{-5}$, $\Delta \phi < 0.008$ deg. achieved



OPERATION HIGHLIGHTS

CW operations with XFEL modules →

Poster : W. Cichalewski

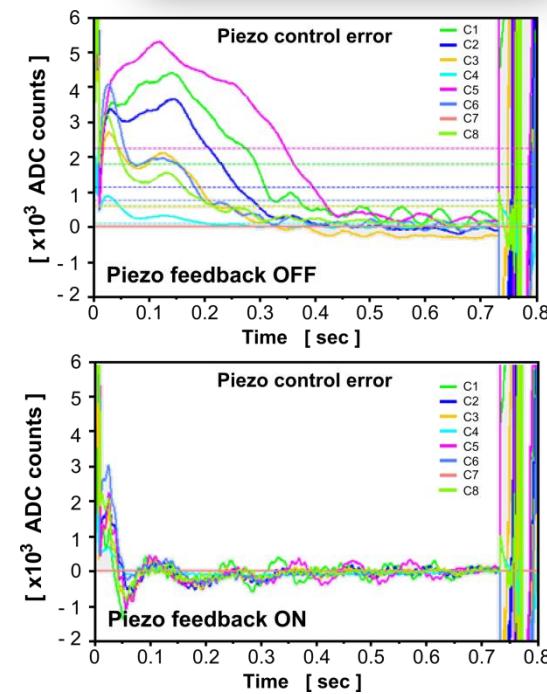
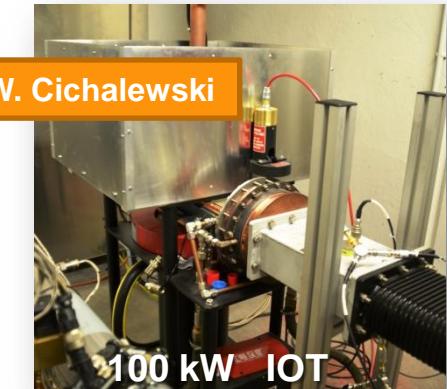
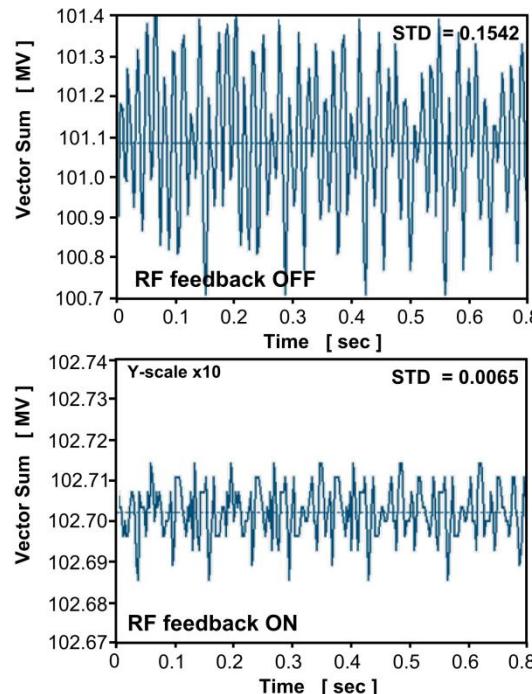


RF FB

- amplitude regulation: 6×10^{-3} % RMS (in-loop)
- phase regulation: 0.0098 deg. RMS (in-loop)

PZ FB

- microphonics suppression from 70 Hz to < 2 Hz
- still problems, more evaluation time required



XFEL and LLRF System: schedule

SCHEDULE

- RF gun commissioning Oct 2013
- LINAC1 (4 cryomodules)
 - ➔ installation
 - ➔ pre-commissioning follows Q1 2014
- 3rd harmonic cavity testing Q1 2014
- 1nd cryo-string (12 cryomodules) Q2 2014
- ...
- 7th cryo-string Feb. 2015
- 8th cryo-string Apr. 2015
- Cold commissioning Jun. 2015
- LLRF ready for 1st beam Sep. 2015
- LLRF ready for nom. beam Feb. 2016



Likely to shift
~ 6 mo.

LLRF MASS PRODUCTION

■ PRR: Project Readiness Review

LLRF MASS PRODUCTION

- PRR: Project Readiness
- Call for tender
- Licensing companies



Deutsches Elektronen-Synchrotron
Bundesforschungszentrum der Helmholtz-Gemeinschaft

Hamburg, July 10th 2013

Specification document for the

Power Supply Module (PSM)

for the European XFEL low level radio frequency external chassis modules

[Summary](#)

This document describes the specifications for the European X-ray free electron laser (XFEL) low level radio frequency (LLRF) subcomponent: the Power Supply Module (PSM). The power supply module (PSM) is a rack-mounted standard 19 inch crate providing DC voltages up to four rack-mounted modules used in the low level radio frequency (LLRF) system of the European X-ray free electron laser (XFEL). The PSM is a fully redundant unit, it can remotely be diagnosed and maintained. Modularity of the individual voltages and hot-swap ability of the redundant supplies are two key elements of the PSM design. Mechanical robustness and electromagnetic shielding are also of paramount importance for this module."

XFEL-LLRF-PSM-Specifications

General parameters

excited by trigger signal, disabled by interlock
maximum excitation of all channels for 10Hz or 25Hz operation
link, at least 100Mb/s

Power output parameters

2 ± 6 µF
maximum amplitude and load), for 10% of the output bandwidth is DC to 3kHz corresponds to 0.1Hz) SNR < -74dB
any channels
max = +85V (clamping level)
full range -2.5 to +2.5V, for current monitoring respond to 1A

Sensor input parameters

LLRF MASS PRODUCTION

- PRR: Project Readiness Review
- Call for tender
- Licensing companies
- Quality control (test stands)



Example :

15 minutes / uDWC
x 300
= 2 weeks

6 ADC saturation level

ADC saturation levels at the plane of the connectors have been measured. ADCs have PASSED the tests.

Channel No.	Saturation level [dBm]	Status
1	9.377488	OK
2	9.326781	OK
3	9.209005	OK
4	9.341573	OK
5	9.125885	OK
6	9.217273	OK
7	9.311251	OK
8	9.303086	OK
9	9.079008	OK
10	9.077752	OK

Table 4: Channel saturation level at ERNI connector plane

7 ADC spectral purity

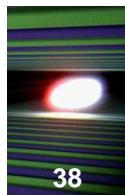
The spurious free dynamic range has been measured. ADCs have PASSED the tests.

Channel No.	SFDR	Status
1	-107.610523	OK
2	-106.659221	OK
3	-107.851373	OK
4	-108.317389	OK
5	-108.254769	OK
6	-108.347070	OK
7	-106.718074	OK
8	-109.275238	OK
9	-107.295380	OK
10	-101.138558	OK

Table 5: ADC 1st harmonic power measurement

8 Channel-to-channel crosstalks

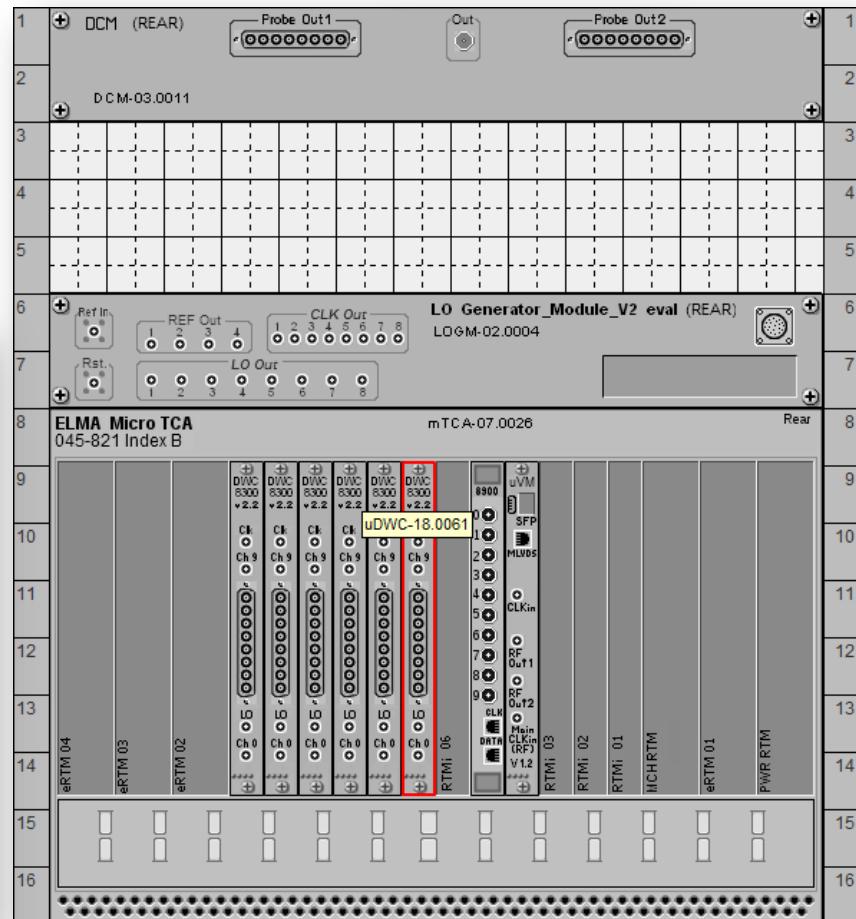
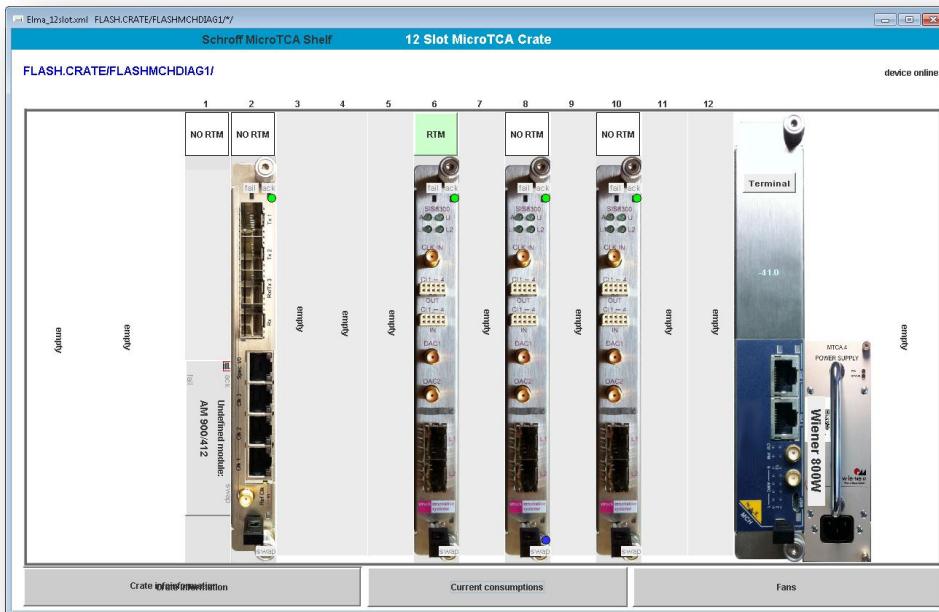
Channel-to-channel crosstalks have been measured. DUT has PASSED the tests.



LLRF MASS PRODUCTION

- PRR: Project Readiness Review
- Call for tender
- Licensing companies
- Quality control (test stands)
- Inventory (database)

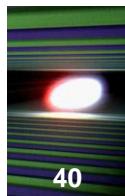
LOGM-
02.0003



LLRF MASS PRODUCTION

- PRR: Project Readiness Review
- Call for tender
- Licensing companies
- Quality control (test stands)
- Inventory (database)
- ...
- RFI: Ready for installation
- RF commissioning
- Ready for beam
- Beam commissioning
- Done ?
- Upgrades



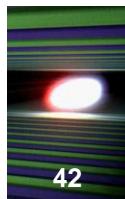


XFEL SNEAK PREVIEW



XFEL SNEAK PREVIEW

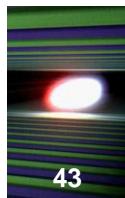




XFEL SNEAK PREVIEW



XFEL SNEAK PREVIEW



XFEL SNEAK PREVIEW



XFEL SNEAK PREVIEW





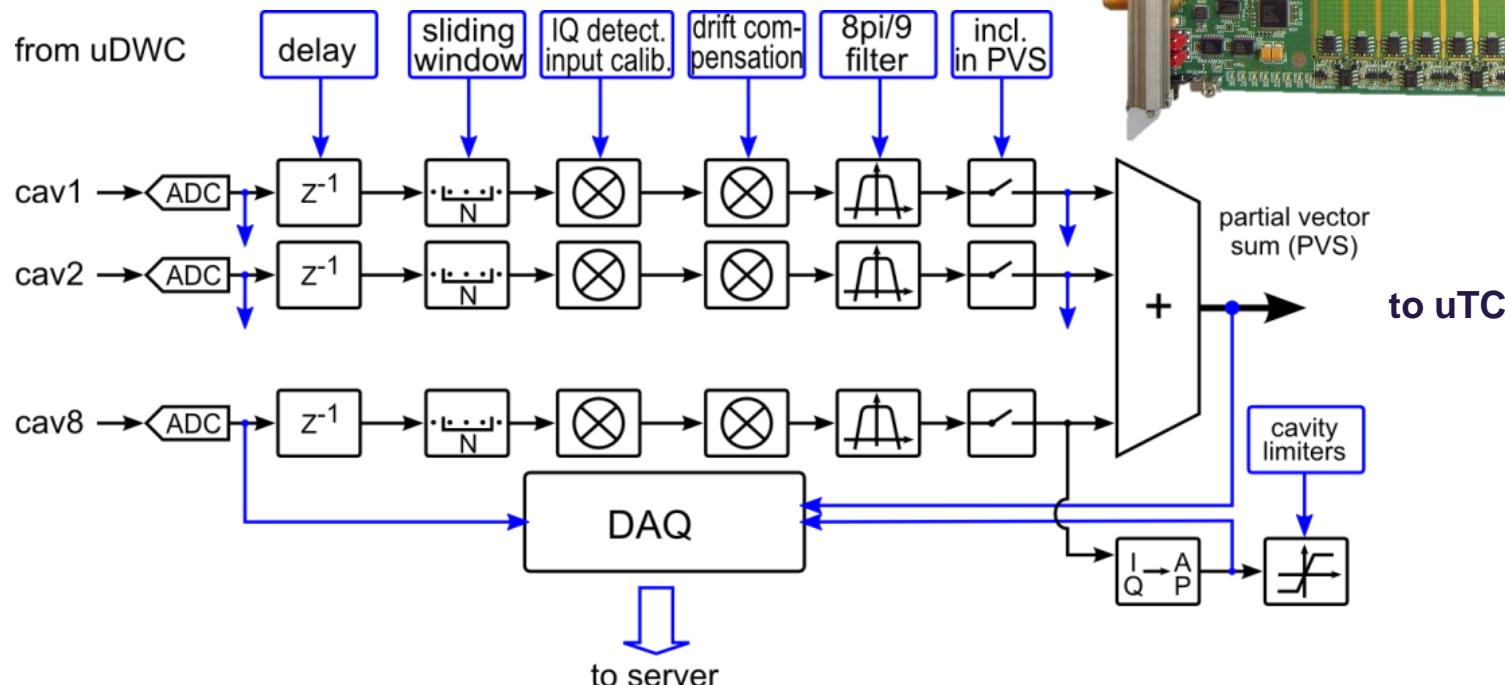
THANK YOU !

BACK UP SLIDES

MTCA.4 core LLRF modules

■ MTCA.4 LLRF digitizer: **SIS8300L**

- 10 ch. digitizer
- Virtex VI
- Application firmware block diagram:

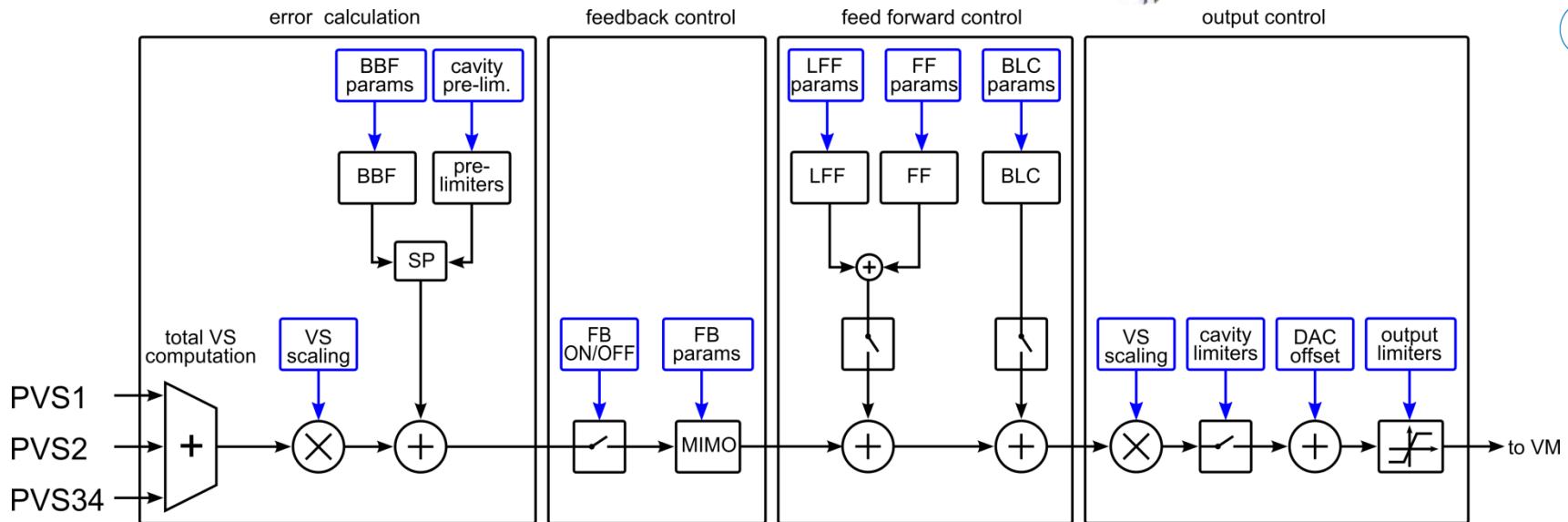
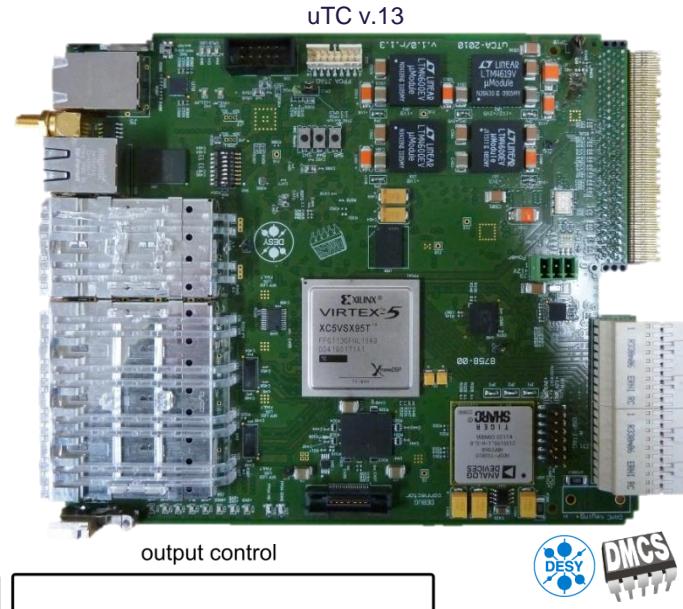


struck innovative
systeme

MTCA.4 core LLRF modules

■ MTCA.4 LLRF controller: uTC

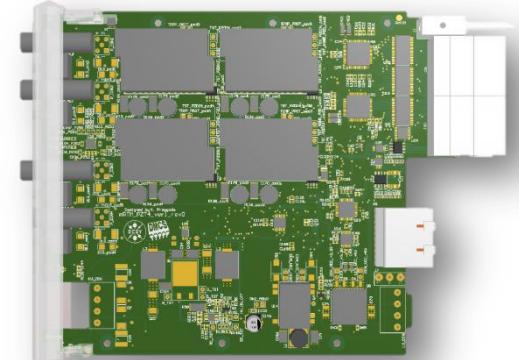
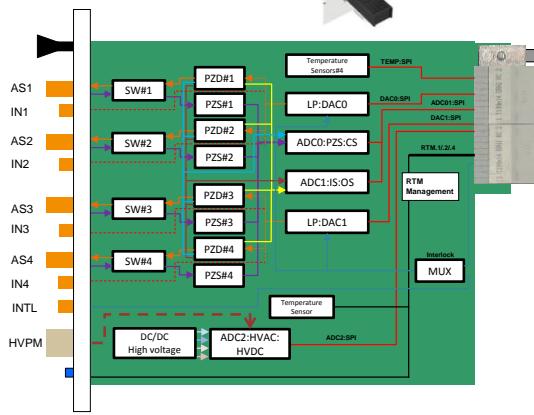
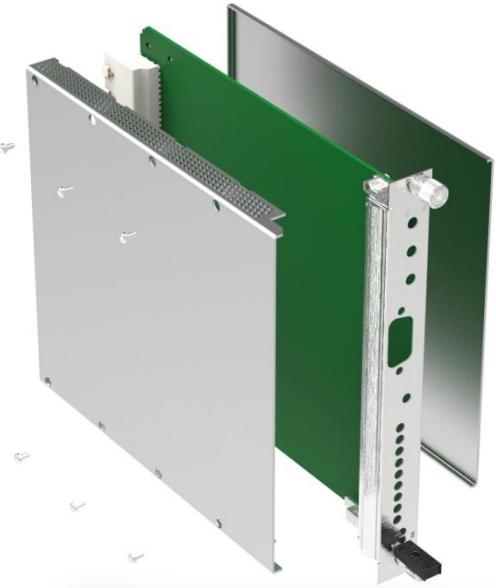
- Current version 1.3 (Virtex V)
- Version 2.0 (Kintex VII)
- 0.5 Tbps processing power
- 8x SPF+ on front panel
- Application firmware block diagram:



MTCA.4 HIGHLIGHTS

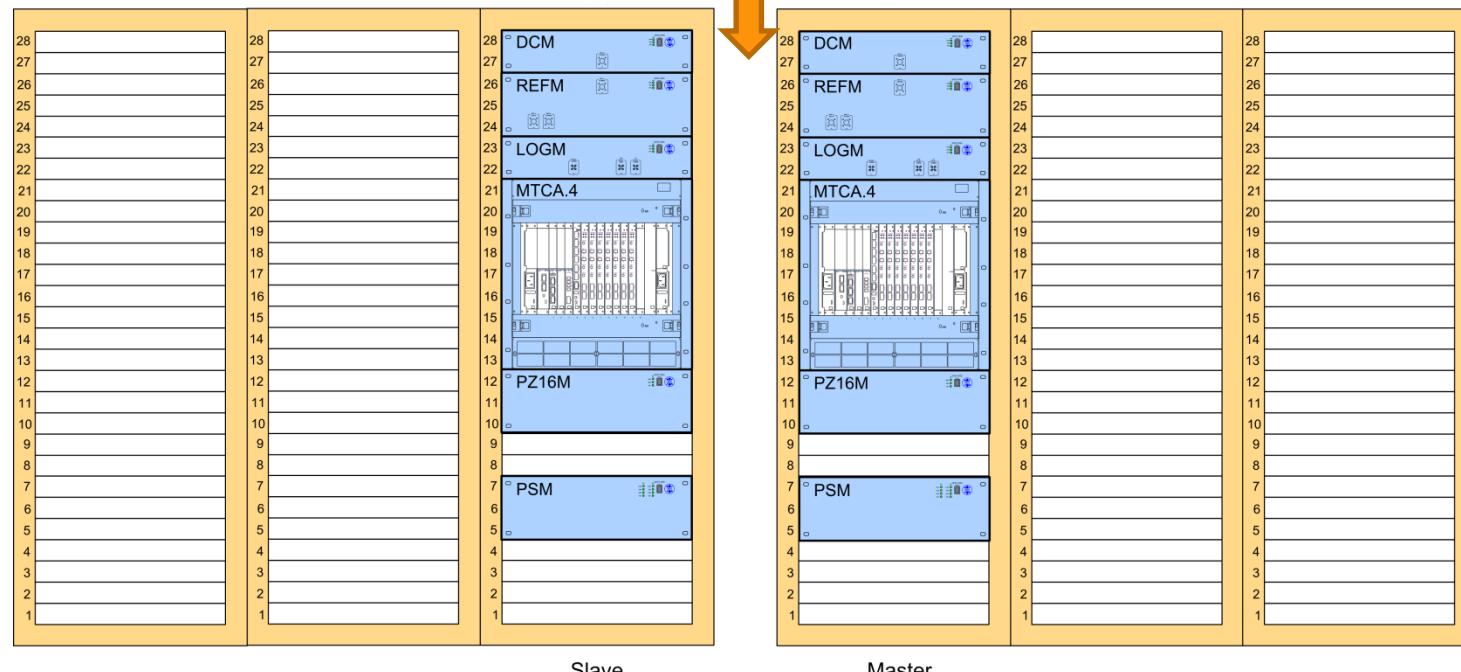
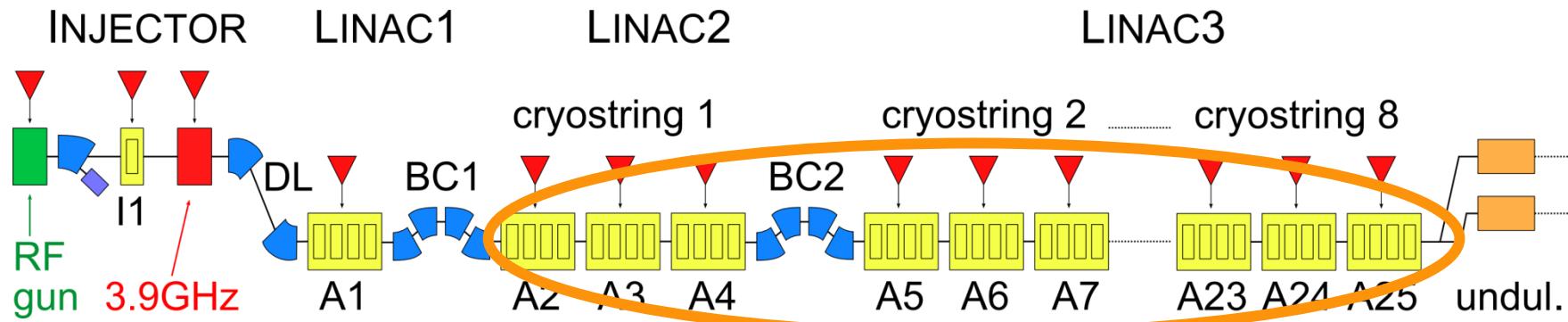
■ MTCA.4 Piezo driver module: uPZT4

XFEL Piezo driver : **PZ16M**

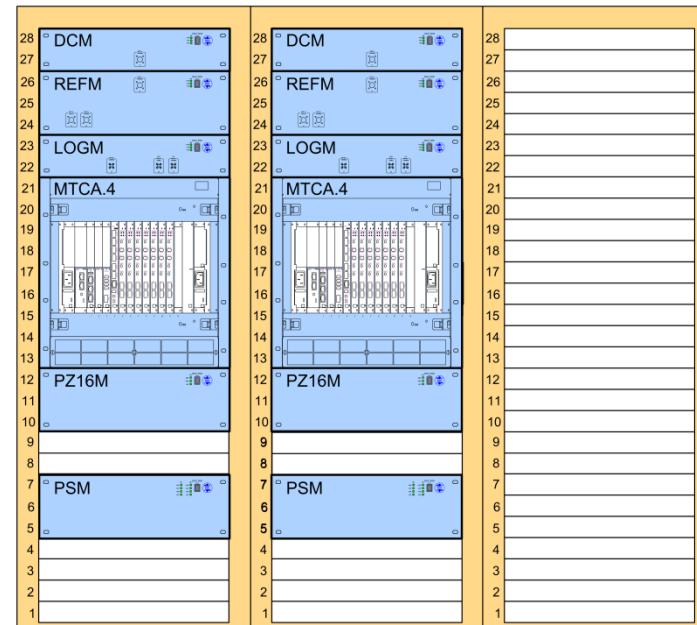
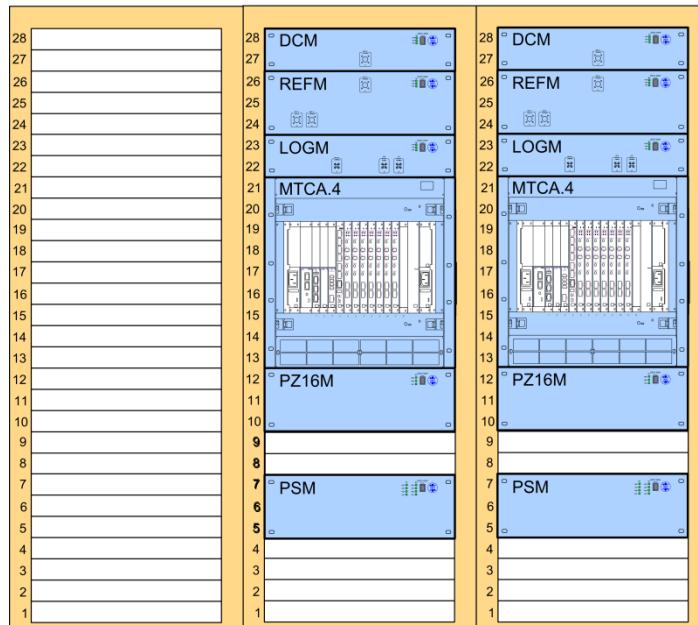
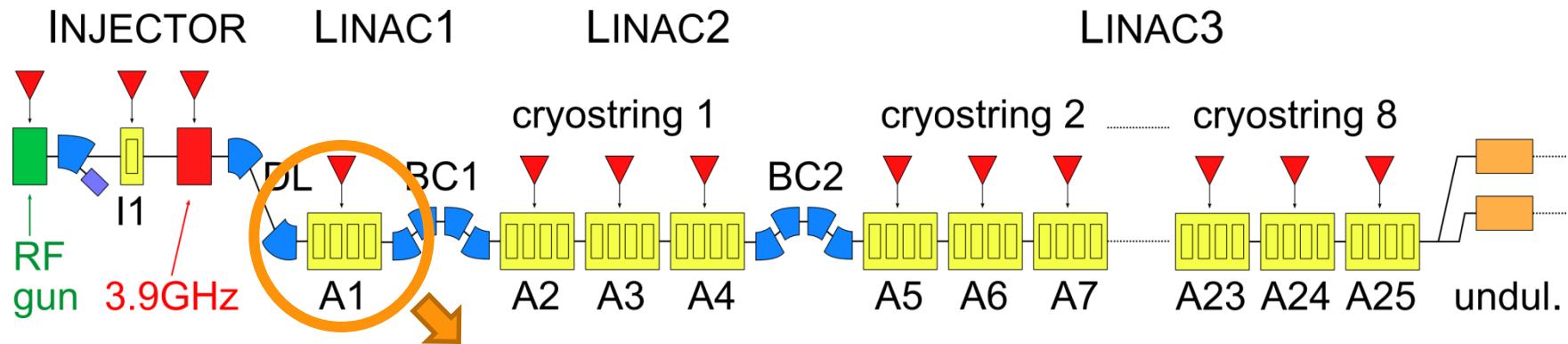


Courtesy K. Przygoda

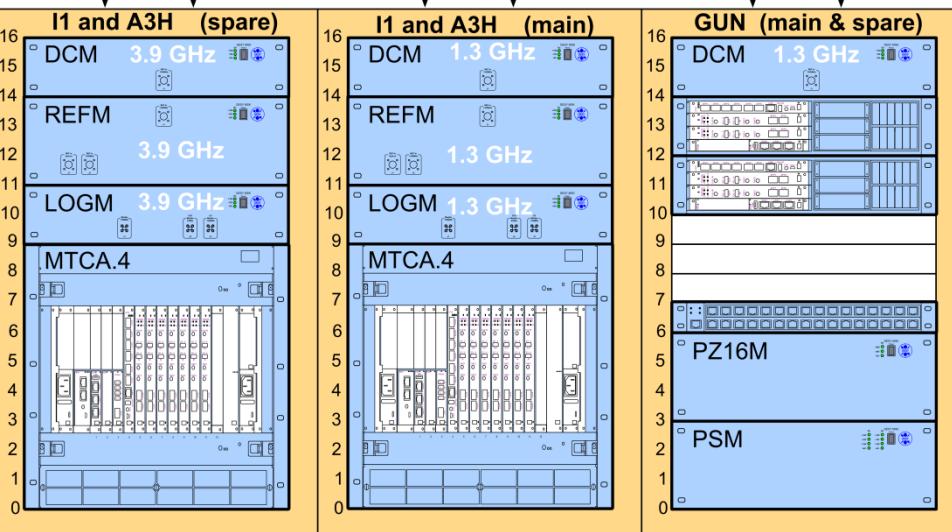
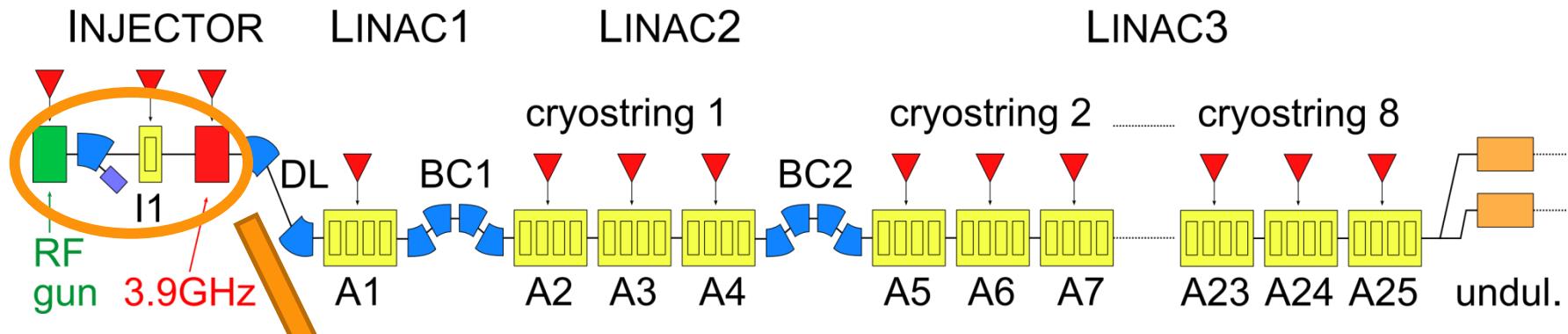
XFEL LLRF : L2 & L3



XFEL LLRF : L1



XFEL LLRF : INJECTOR



- **DCM**: Drift Calibration Module
- **REFM**: Reference synchronization and distribution Module
- **LOGM**: Local Oscillator Generation and distribution Module
- **MTCA.4**
- **PZ16M**: Piezo controller Module (16 cavities)
- **PSM**: Power Supply Module

LLRF Installations



LLRF Installations



LLRF : MASTER OSCILLATOR & REFERENCE

■ Master oscillator

- room layout in XTIN done
- acclimatized racks ordered



■ Reference line distribution

- on-going prototypes performance tests
- final design done

